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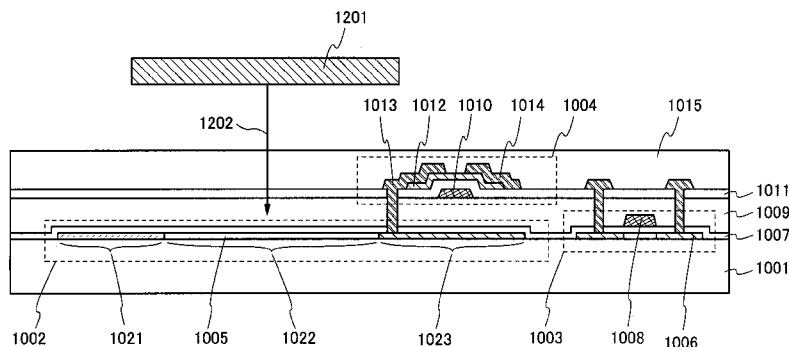
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(54) Title: SEMICONDUCTOR DEVICE

FIG. 19



(57) Abstract: An object of the invention is to improve the accuracy of light detection in a photosensor, and to increase the light-receiving area of the photosensor. The photosensor includes: a light-receiving element which converts light into an electric signal; a first transistor which transfers the electric signal; and a second transistor which amplifies the electric signal. The light-receiving element includes a silicon semiconductor, and the first transistor includes an oxide semiconductor. The light-receiving element is a lateral-junction photodiode, and an n-region or a p-region included in the light-receiving element overlaps with the first transistor.

## DESCRIPTION

## SEMICONDUCTOR DEVICE

## 5 TECHNICAL FIELD

[0001]

The technical field relates to a photosensor, a semiconductor device including the photosensor, and a driving method thereof.

## 10 BACKGROUND ART

[0002]

In recent years, attention has been driven to semiconductor devices provided with light-detecting sensors (also referred to as photosensors) (see Patent Document 1).

[0003]

15 Semiconductor devices provided with photosensors include CCD image sensors, CMOS image sensors, and the like. Such image sensors are used, for example, in electronic apparatuses like digital still cameras or cellular phones. Further, as semiconductor devices including photosensors in their display portions, touch panels and the like have been developed.

20 [0004]

In a semiconductor device including a photosensor, light emitted from an object to be detected or external light reflected by the object to be detected is detected directly by the photosensor or condensed by an optical lens or the like and then detected.

25 [Reference]

[Patent Document]

[0005]

[Patent Document 1] Japanese Published Patent Application No. 2001-292276

## 30 DISCLOSURE OF INVENTION

[0006]

An object of the present invention is to improve the accuracy of light detection

in a photosensor.

[0007]

It is another object to provide a new layout or structure of a circuit of a photosensor. In particular, an object of the present invention is to increase a  
5 light-receiving area.

[0008]

One embodiment of the present invention is a semiconductor device including: a light-receiving element which converts light into an electric signal; a first transistor which transfers the electric signal; and a second transistor which amplifies the electric  
10 signal. The light-receiving element includes a silicon semiconductor, and the first transistor includes an oxide semiconductor.

[0009]

The light-receiving element is a lateral-junction photodiode, and an n-region or a p-region included in the light-receiving element overlaps with the first transistor.

15 [0010]

The light-receiving element is formed over the same surface as the second transistor.

[0011]

A wiring formed over a light-receiving region of the light-receiving element is  
20 made of a light-transmitting material.

[0012]

In this specification, the semiconductor device refers to an element having a semiconductor property, and all the object including the element. For example, a display device including a transistor is simply referred to as a semiconductor device in  
25 some cases.

[0013]

In the photosensor, an oxide semiconductor is used for the first transistor which transfers an electric signal, resulting in a reduction in the leakage current of the first transistor in the off state and an improvement in the accuracy of detecting light.

30 [0014]

Furthermore, since the n-region or the p-region included in the light-receiving element overlaps with the first transistor, the light-receiving area of the light-receiving

element can be increased; accordingly, high light sensitivity can be realized and light can be detected with high accuracy.

[0015]

In addition, since the wiring formed over the light-receiving region is made of  
5 a light-transmitting material, the light-receiving area can be increased; accordingly, high  
light sensitivity can be realized and light can be detected with high accuracy.

#### BRIEF DESCRIPTION OF DRAWINGS

[0016]

10 In the accompanying drawings:  
FIG. 1 illustrates an example of a circuit of a semiconductor device;  
FIG. 2 illustrates an example of a layout of a semiconductor device;  
FIG. 3 illustrates an example of a layout of a semiconductor device;  
FIG. 4 illustrates an example of a layout of a semiconductor device;  
15 FIG. 5 illustrates an example of a layout of a semiconductor device;  
FIG. 6 illustrates an example of a layout of a semiconductor device;  
FIG. 7 illustrates an example of a circuit of a semiconductor device;  
FIG. 8 illustrates an example of a layout of a semiconductor device;  
FIG. 9 illustrates an example of a layout of a semiconductor device;  
20 FIG. 10 illustrates an example of a layout of a semiconductor device;  
FIG. 11 illustrates an example of a layout of a semiconductor device;  
FIG. 12 illustrates an example of a layout of a semiconductor device;  
FIG. 13 illustrates an example of a circuit of a semiconductor device;  
FIG. 14 illustrates an example of a layout of a semiconductor device;  
25 FIG. 15 illustrates an example of a layout of a semiconductor device;  
FIG. 16 illustrates an example of a layout of a semiconductor device;  
FIG. 17 illustrates an example of a layout of a semiconductor device;  
FIG. 18 illustrates an example of a layout of a semiconductor device;  
FIG. 19 illustrates an example of a cross-sectional structure of a semiconductor  
30 device;  
FIG. 20 illustrates an example of a cross-sectional structure of a semiconductor  
device;

FIG. 21 illustrates an example of a circuit of a semiconductor device;

FIG. 22 illustrates an example of a layout of a semiconductor device;

FIG. 23 illustrates an example of a cross-sectional structure of a semiconductor device;

5 FIG. 24 illustrates an example of a cross-sectional structure of a semiconductor device;

FIG. 25 illustrates an example of a semiconductor device;

FIG. 26 illustrates an example of a semiconductor device;

FIGS. 27A to 27F illustrate examples of a circuit of a semiconductor device;

10 FIG. 28 is a graph showing characteristics of a semiconductor device; and

FIG. 29 is a timing chart.

## BEST MODE FOR CARRYING OUT THE INVENTION

[0017]

15 Embodiments will be described in detail below with reference to drawings. Note that the following embodiments can be implemented in many different modes, and it is apparent to those skilled in the art that modes and details can be modified in various ways without departing from the spirit and scope of the present invention. Therefore, the present invention is not construed as being limited to the description of the

20 embodiments. Note that in all the drawings for explaining the embodiments, like portions or portions having a similar function are denoted by like reference numerals, and the description thereof is omitted.

[0018]

(Embodiment 1)

25 In this embodiment, the circuit and layout of a semiconductor device will be described.

[0019]

FIG. 1 is an example of a circuit diagram of a photosensor.

[0020]

30 The photosensor includes a photodiode 100, a transistor 101, a transistor 102, a transistor 103, and a transistor 104.

[0021]

The photodiode 100 has a function of converting light into an electric signal (an electric charge). Other than the photodiode, a light-receiving element having this function, such as a phototransistor, can also be used.

[0022]

5 The transistor 101 has a function of transferring the converted electric signal to a gate of the transistor 103. Thus, the transistor 101 is also referred to as a transferring transistor.

[0023]

10 The transistor 102 has a function of controlling a gate potential of the transistor 103 so that the gate potential is reset to a predetermined potential. Thus, the transistor 102 is also referred to as a reset transistor.

[0024]

15 The transistor 103 has a function of amplifying the transferred electric signal and generating an output signal. Thus, the transistor 103 is also referred to as an amplifying transistor. Note that the amplification means here that a current value between a source and a drain is controlled with a gate potential.

[0025]

20 The transistor 104 has a function of controlling the reading of the output signal. For example, among a plurality of photosensors, the output from a predetermined photosensor is selected by the transistor 104; thus, the transistor 104 is also referred to as a selection transistor.

[0026]

25 In the circuit of FIG. 1, a gate of the transistor 101 is electrically connected to a wiring 106 (also referred to as a charge control signal line), one of a source and a drain of the transistor 101 is electrically connected to one electrode of the photodiode 100, and the other of the source and the drain of the transistor 101 is electrically connected to one of a source and a drain of the transistor 102 and the gate of the transistor 103. A gate of the transistor 102 is electrically connected to a wiring 107 (also referred to as a reset signal line), and the other of the source and the drain of the transistor 102 is electrically connected to a wiring 108 (also referred to as a power supply line). One of  
30 a source and a drain of the transistor 103 is electrically connected to the wiring 108, and the other thereof is electrically connected to one of a source and a drain of the transistor

104. A gate of the transistor 104 is electrically connected to a wiring 109 (also referred to as a selection signal line), and the other of the source and the drain of the transistor 104 is electrically connected to a wiring 110 (also referred to as an output line). Note that the other electrode of the photodiode 100 is electrically connected to a wiring 120. The potential of the wiring 120 can be set to a desired potential: either a  
5 fixed potential (e.g., the ground potential) or a variable potential.

[0027]

FIG. 2 is an example of the layout of the photosensor.

[0028]

10 The photosensor includes a light-receiving element (the photodiode 100) and the four transistors 101 to 104. The photodiode 100 is a lateral-junction PIN photodiode, in which an n-region 201, an i-region 202, and a p-region 203 are formed over the same surface. The transistor 101 and the transistor 102 are formed over the n-region 201. The light-receiving area can be increased with the layout in which part  
15 or the whole of the transistor 101 overlaps with the n-region 201 that is not a light-receiving region.

[0029]

Note that the transistor 101 may be formed over the p-region 203 that is not a light-receiving region. The photodiode 100 can be a PN photodiode without an  
20 i-region.

[0030]

In this embodiment, an oxide semiconductor is preferably used for the transistor 101 and the transistor 102. Such a structure allows reducing the leakage of the electric signal, which has been supplied to the gate of the transistor 103, from the  
25 transistor 101 and the transistor 102. This is because a transistor using an oxide semiconductor has a low leakage current in the off state. Consequently, light can be detected with high accuracy. The structure is particularly effective in the case where it takes a long time between light reception and reading out.

[0031]

30 The structure is also effective in a semiconductor device including a plurality of photosensors (e.g., FIG. 25 or FIG. 26), in the case where the time between light reception and reading out varies from photosensor to photosensor. There are structures

where light is simultaneously received in all the photosensors and reading is sequentially performed for each line.

[0032]

Note that the photodiode 100, the transistor 103, and the transistor 104 are formed by making use of the same semiconductor material. Since the photodiode 100, the transistor 103, and the transistor 104 can be formed over the same surface in the same process, cost reduction can be achieved. When a semiconductor with high mobility is used, the quantum efficiency of the photodiode 100 can be increased, and amplification by the transistor 103 and reading by the transistor 104 can be performed efficiently. Here, a crystalline semiconductor is used. In particular, single crystal silicon is preferably used, though other semiconductors, such as an amorphous semiconductor or an oxide semiconductor, can be used as needed.

[0033]

FIG. 3 to FIG. 6 are other examples of the layout of the photosensor, each of which is different from FIG. 2 in the light-receiving area of the photodiode 100.

[0034]

Note that in FIG. 3, FIG. 5 and FIG. 6, a light-transmitting material is used for a wiring (a conductive layer 130) that overlaps with a light-receiving region of the photodiode 100 (here, the i-region 202). Light passes through the conductive layer 130 and enters the i-region 202; accordingly, the light-receiving area can be increased.

[0035]

As the light-transmitting material, it is possible to use, for example, indium tin oxide (ITO), indium tin oxide containing silicon oxide (ITSO), organoindium, organotin, zinc oxide, indium zinc oxide (IZO), zinc oxide containing gallium, tin oxide, indium oxide containing tungsten oxide, indium zinc oxide containing tungsten oxide, indium oxide containing titanium oxide, or indium tin oxide containing titanium oxide.

[0036]

Furthermore, in FIG. 3, the transistor 101 and the transistor 102 are formed to overlap with the i-region 202 of the photodiode. This structure makes use of the transmitting property of the oxide semiconductor: light passes through the transistor 101, the transistor 102, and the conductive layer 130 and enters the i-region 202. Accordingly, the light-receiving area can be increased.

[0037]

When the light-receiving area is increased in the aforementioned manner, light sensitivity can be increased and thus light can be detected with high accuracy.

[0038]

5 This embodiment can be implemented in appropriate combination with the other embodiments.

[0039]

(Embodiment 2)

10 This embodiment shows an example of the circuit and layout of the semiconductor device, which is different from the example shown in Embodiment 1.

[0040]

FIG. 7 is an example of the circuit diagram of the photosensor, in which the transistor 102 of FIG. 1 is omitted.

[0041]

15 FIG. 8 to FIG. 12 are examples of the layout of the circuit in FIG. 7, in which the transistor 102 of FIG. 2 to FIG. 6 is omitted, respectively. The light-receiving area can be increased because of a reduction in the number of elements.

[0042]

20 FIG. 13 is an example of the circuit of the photosensor, in which the transistor 102 and the transistor 104 of FIG. 1 are omitted.

[0043]

25 FIG. 14 to FIG. 18 are examples of the layout of the circuit in FIG. 13, in which the transistor 102 and the transistor 104 of FIG. 2 to FIG. 6 are omitted, respectively. The light-receiving area can be further increased because of a reduction in the number of elements.

[0044]

This embodiment can be implemented in appropriate combination with the other embodiments.

[0045]

30 (Embodiment 3)

In this embodiment, a cross-sectional structure of the semiconductor device will be described.

[0046]

FIG. 19 is a cross-sectional view of the photosensor illustrated in FIG. 1 to FIG. 18.

[0047]

5 In FIG. 19, a photodiode 1002, a transistor 1003, and a transistor 1004 are provided over a substrate 1001 having an insulating surface. The photodiode 1002, the transistor 1003, and the transistor 1004 respectively illustrate an example of the cross-sectional structure of the photodiode 100, the transistor 103, and the transistor 101 illustrated in FIG. 1 to FIG. 18.

10 [0048]

Light 1202 emitted from an object to be detected 1201, light 1202 reflected by the object to be detected 1201 (such as external light), or light 1202 emitted from the inside of the device and reflected by the object to be detected 1201 enters the photodiode 1002. An object to be detected may be provided on the substrate 1001 side so that an image thereof is taken.

[0049]

The substrate 1001 can be an insulating substrate (e.g., a glass substrate or a plastic substrate), the insulating substrate on which an insulating film (e.g., a silicon oxide film or a silicon nitride film) is formed, a semiconductor substrate (e.g., a silicon substrate) on which the insulating film is formed, or a metal substrate (e.g., an aluminum substrate) on which the insulating film is formed.

[0050]

25 The photodiode 1002 is a lateral-junction PIN photodiode and includes a semiconductor film 1005. The semiconductor film 1005 includes a region having p-type conductivity (a p-region 1021), a region having i-type conductivity (i-region 1022), and a region having n-type conductivity (n-region 1023). Note that the photodiode 1002 may be a PN photodiode.

[0051]

30 The lateral-junction PIN or PN photodiode can be formed by adding a p-type impurity and an n-type impurity to predetermined regions of the semiconductor film 1005.

[0052]

In the photodiode 1002, a single crystal semiconductor (e.g., single crystal silicon) with few crystal defects is preferably used for the semiconductor film 1005 so as to improve the proportion of an electric signal generated from incident light (the quantum efficiency).

5 [0053]

The transistor 1003 is a top-gate thin film transistor and includes a semiconductor film 1006, a gate insulating film 1007, and a gate electrode 1008.

[0054]

10 The transistor 1003 has a function of converting an electric signal supplied from the photodiode 1002 into an output signal. Therefore, a single crystal semiconductor (e.g., single crystal silicon) is preferably used for the semiconductor film 1006 to obtain a transistor with high mobility.

[0055]

15 An example of forming the semiconductor film 1005 and the semiconductor film 1006 with the use of a single crystal semiconductor will be described. A damaged region is formed at a desired depth of a single crystal semiconductor substrate (e.g., a single crystal silicon substrate) by ion irradiation or the like. The single crystal semiconductor substrate and the substrate 1001 are bonded to each other with an insulating film interposed therebetween; then, the single crystal semiconductor substrate  
20 is split along the damaged region, whereby a semiconductor film is formed over the substrate 1001. The semiconductor film is processed (patterned) into a desired shape by etching or the like, so that the semiconductor film 1005 and the semiconductor film 1006 are formed. Since the semiconductor film 1005 and the semiconductor film 1006 can be formed in the same process, cost reduction can be realized. In this manner, the  
25 photodiode 1002 and the transistor 1003 can be formed on the same surface.

[0056]

Note that an amorphous semiconductor, a microcrystal semiconductor, a polycrystalline semiconductor, an oxide semiconductor, or the like can also be used for the semiconductor film 1005 and the semiconductor film 1006. In particular, a single  
30 crystal semiconductor is preferably used to obtain a transistor with high mobility. As the semiconductor material, it is preferable to use a silicon semiconductor such as silicon or silicon germanium, the crystallinity of which can be easily increased.

[0057]

Here, the semiconductor film 1005 is preferably made thick in order to improve the quantum efficiency of the photodiode 1002. Further, the semiconductor film 1006 is preferably made thin in order to improve the electrical properties such as the S value  
5 of the transistor 1003. In that case, the semiconductor film 1005 is only required to be made thicker than the semiconductor film 1006.

[0058]

A crystal semiconductor is also preferably used for the transistor 104 in FIG. 2 to FIG. 12 so as to obtain a transistor with high mobility. By using the same  
10 semiconductor material as the transistor 1003, the transistor 104 can be formed in the same process as the transistor 1003, resulting in cost reduction.

[0059]

Note that the gate insulating film 1007 is formed as a single layer or stacked layers using a silicon oxide film, a silicon nitride film, or the like. The gate insulating  
15 film 1007 can be formed by plasma CVD or sputtering.

[0060]

Note that the gate electrode 1008 is formed as a single layer or stacked layers using a metal material such as molybdenum, titanium, chromium, tantalum, tungsten, aluminum, copper, neodymium, or scandium, or an alloy material including any of these  
20 materials as a main component. The gate electrode 1008 can be formed by sputtering or vacuum evaporation.

[0061]

The transistor 1003 can be a bottom-gate transistor, and can have a channel-stop structure or a channel-etched structure.

25 [0062]

The transistor 1004 is a bottom-gate inverted-staggered thin film transistor and includes a gate electrode 1010, a gate insulating film 1011, a semiconductor film 1012, an electrode 1013, and an electrode 1014. An insulating film 1015 is provided over the transistor 1004.

30 [0063]

A feature of the structure is that the transistor 1004 is formed over the photodiode 1002 and the transistor 1003 with an insulating film 1009 interposed

therebetween. When the transistor 1004 and the photodiode 1002 are formed on different layers in this manner, the area of the photodiode 1002 can be increased.

[0064]

Furthermore, part or the whole of the transistor 1004 is preferably formed to overlap with either the n-region 1023 or the p-region 1021 of the photodiode 1002, whereby the light-receiving area of the photodiode 1002 can be increased. Also in the case of a PN photodiode, part or the whole of the transistor 1004 is preferably formed to overlap with either the n-region or the p-region.

[0065]

The function of the transistor 1004 is to accumulate an electric signal supplied from the photodiode 1002 in the gate of the transistor 1003 and to retain the electric signal. Therefore, an oxide semiconductor is preferably used for the semiconductor film 1012 so that the transistor 1004 has an extremely low off-current.

[0066]

It is also preferable to use an oxide semiconductor for the transistor 102 in FIG. 1 to FIG. 6 so that the transistor 102 has a low off-current. By using the same semiconductor material as the transistor 1004, the transistor 102 can be formed over the same surface and in the same process as the transistor 1004, resulting in cost reduction. When the transistor 1004 is formed to overlap with the n-region 1023 or the p-region 1021, an increase in light-receiving area can be achieved.

[0067]

An example of forming the semiconductor film 1012 using an oxide semiconductor will be described below.

[0068]

One of the factors that increase the off-current of a transistor is an impurity such as hydrogen (e.g., hydrogen, water, or a hydroxyl group) contained in an oxide semiconductor. Hydrogen or the like might be a carrier supplier (donor) in an oxide semiconductor, which causes electric current even in the off state. That is, an oxide semiconductor containing a large amount of hydrogen or the like becomes an n-type oxide semiconductor.

[0069]

Thus, in the manufacturing method shown below, the amount of hydrogen in an

oxide semiconductor is reduced as much as possible and the concentration of oxygen which is a constituent element is increased, whereby the oxide semiconductor is highly purified. The highly-purified oxide semiconductor is an intrinsic or substantially intrinsic semiconductor, resulting in a reduction in off-current.

5 [0070]

First, an oxide semiconductor film is formed over the insulating film 1009 by sputtering.

[0071]

As a target used for forming the oxide semiconductor film, a target of a metal  
10 oxide containing zinc oxide as a main component can be used. For example, it is possible to use a target with a composition ratio of  $\text{In}_2\text{O}_3$ :  $\text{Ga}_2\text{O}_3$ :  $\text{ZnO}$  = 1:1:1, that is, In: Ga: Zn = 1:1:0.5. It is also possible to use a target with a composition ratio of In: Ga: Zn = 1:1:1 or a composition ratio of In: Ga: Zn = 1:1:2. Further, a target which includes  $\text{SiO}_2$  at 2 wt% to 10 wt% inclusive can be used.

15 [0072]

Note that the oxide semiconductor film can be formed in a rare gas (typically, argon) atmosphere, an oxygen atmosphere, or a mixed atmosphere of a rare gas and oxygen. Here, a sputtering gas used for forming the oxide semiconductor film is a high-purity gas in which impurities such as hydrogen, water, hydroxyl groups, or  
20 hydride are reduced down to concentrations of the order of ppm or ppb levels.

[0073]

The oxide semiconductor film is formed by introducing a sputtering gas from which hydrogen and moisture are removed, while removing moisture remaining in a treatment chamber. In order to remove moisture remaining in the treatment chamber,  
25 an entrapment vacuum pump is preferably used. For example, a cryopump, an ion pump, or a titanium sublimation pump is preferably used.

[0074]

The thickness of the oxide semiconductor film can be 2 nm to 200 nm inclusive, preferably 5 nm to 30 nm inclusive. Then, the oxide semiconductor film is processed  
30 (patterned) into a desired shape by etching or the like, whereby the semiconductor film 1012 is formed.

[0075]

Although an In-Ga-Zn-O is used for the oxide semiconductor film in the above example, the following oxide semiconductors can also be used: In-Sn-Ga-Zn-O, In-Sn-Zn-O, In-Al-Zn-O, Sn-Ga-Zn-O, Al-Ga-Zn-O, Sn-Al-Zn-O, In-Zn-O, Sn-Zn-O, Al-Zn-O, Zn-Mg-O, Sn-Mg-O, In-Mg-O, In-O, Sn-O, Zn-O, and the like. The oxide semiconductor film may contain Si. Further, the oxide semiconductor film may be amorphous or crystalline. Further, the oxide semiconductor film may be non-single-crystal or single crystal.

[0076]

As the oxide semiconductor film, a thin film represented by  $\text{InMO}_3(\text{ZnO})_m$  ( $m > 0$ ) can also be used. Here, M denotes one or more of metal elements selected from Ga, Al, Mn, and Co. For example, M can be Ga, Ga and Al, Ga and Mn, or Ga and Co.

[0077]

Next, first heat treatment is performed on the oxide semiconductor film (the semiconductor film 1012). The temperature of the first heat treatment is higher than or equal to 400 °C and lower than or equal to 750 °C, preferably higher than or equal to 400 °C and lower than the strain point of the substrate.

[0078]

Through the first heat treatment, hydrogen, water, hydroxyl groups, and the like can be removed from the oxide semiconductor film (the semiconductor film 1012) (dehydrogenation treatment). The dehydrogenation treatment through the first heat treatment is significantly effective because such impurities become a donor in the oxide semiconductor film and increase the off-current of the transistor.

[0079]

Note that the first heat treatment can be performed in an electric furnace. Alternatively, heat conduction or heat radiation from a heating element such as a resistance heating element can be used for the first heat treatment. In that case, an RTA (rapid thermal anneal) apparatus such as a GRTA (gas rapid thermal anneal) apparatus or an LRTA (lamp rapid thermal anneal) apparatus can be used.

[0080]

An LRTA apparatus is an apparatus for heating an object to be processed by radiation of light (an electromagnetic wave) emitted from a lamp such as a halogen lamp, a metal halide lamp, a xenon arc lamp, a carbon arc lamp, a high-pressure sodium lamp, or a high-pressure mercury lamp.

5 [0081]

A GRTA apparatus is an apparatus for heat treatment using a high-temperature gas. As the gas, an inert gas (typically, a rare gas such as argon) or a nitrogen gas can be used. The use of the GRTA apparatus is particularly effective because high-temperature heat treatment in a short time is enabled.

10 [0082]

The first heat treatment may be performed before the patterning of the oxide semiconductor film, after the formation of the electrode 1013 and the electrode 1014, or after the formation of the insulating film 1015. However, the first heat treatment is preferably performed before the formation of the electrode 1013 and the electrode 1014 so that the electrodes are not damaged by the first heat treatment.

15

[0083]

During the first heat treatment, oxygen deficiencies might be generated in the oxide semiconductor. Therefore, after the first heat treatment, oxygen is preferably introduced to the oxide semiconductor (treatment for supplying oxygen) so that oxygen which is a constituent element is highly purified.

20

[0084]

Specifically, as an example the treatment for supplying oxygen, the first heat treatment is followed by second heat treatment in an oxygen atmosphere or an atmosphere containing nitrogen or oxygen (for example, the volume ratio of nitrogen to oxygen is 4 to 1). Alternatively, plasma treatment may be performed in an oxygen atmosphere, whereby the oxygen concentration in the oxide semiconductor film can be increased and the oxide semiconductor film can be highly purified. The temperature of the second heat treatment is higher than or equal to 200 °C and lower than or equal to 400 °C, preferably higher than or equal to 250 °C and lower than or equal to 350 °C.

25

30 [0085]

As another example of the treatment for supplying oxygen, an oxide insulating

film (the insulating film 1015) such as a silicon oxide film is formed on and in contact with the semiconductor film 1012, and then a third heat treatment is performed. Oxygen in the insulating film 1015 moves to the semiconductor film 1012 to increase the oxygen concentration in the oxide semiconductor, whereby the oxide semiconductor film can be highly purified. The temperature of the third heat treatment is higher than or equal to 200 °C and lower than or equal to 400 °C, preferably higher than or equal to 250 °C and lower than or equal to 350 °C. Note that also in the case of a top-gate transistor, the oxide semiconductor can be highly purified in such a manner that a gate insulating film on and in contact with the semiconductor film 1012 is formed of a silicon oxide film or the like and similar heat treatment is performed.

[0086]

As described above, the oxide semiconductor film can be highly purified through the treatment for supplying oxygen such as the second heat treatment or the third heat treatment after the dehydrogenation treatment by the first heat treatment. When being highly purified, the oxide semiconductor can be made intrinsic or substantially intrinsic, resulting in a reduction in the off-current of the transistor 1004.

[0087]

Note that the insulating film 1009 is a single layer or stacked layers using a silicon oxide film, a silicon nitride film, or the like, and is formed over the photodiode 1002 and the transistor 1003. The insulating film 1009 can be formed by plasma CVD or sputtering. The insulating film 1009 may also be formed of a resin film such as a polyimide film by coating or the like.

[0088]

The gate electrode 1010 is formed as a single layer or stacked layers using a metal material such as molybdenum, titanium, chromium, tantalum, tungsten, aluminum, copper, neodymium, or scandium, or an alloy material including any of these materials as a main component. The gate electrode 1010 can be formed by sputtering or vacuum evaporation.

[0089]

The gate insulating film 1011 is formed as a single layer or stacked layers using a silicon oxide film, a silicon nitride film, or the like. The gate insulating film 1011

may be formed by plasma CVD or sputtering.

[0090]

The electrode 1013 and the electrode 1014, which are formed over the gate insulating film 1011 and the semiconductor film 1012, each are a single layer or stacked  
5 layers using a metal such as molybdenum, titanium, chromium, tantalum, tungsten, aluminum, copper, or yttrium, an alloy material including any of these materials as a main component, or a metal oxide having conductivity such as indium oxide. The electrode 1013 and the electrode 1014 can be formed by sputtering or vacuum evaporation. Here, it is preferable that the electrode 1013 be electrically connected to  
10 the n-region 1023 of the photodiode 1002 through a contact hole formed in the gate insulating film 1007, the insulating film 1009, and the gate insulating film 1011. It is also preferable that the electrode 1013 and the electrode 1014 be formed to overlap with the gate electrode 1010, whereby the current drive capability of the transistor 1004 can be increased. Such a structure is particularly effective in the case of using an intrinsic  
15 or substantially intrinsic oxide semiconductor.

[0091]

The highly-purified oxide semiconductor and a transistor using the same will be described in detail below.

[0092]

20 As an example of the highly-purified oxide semiconductor, there is an oxide semiconductor whose carrier concentration is less than  $1 \times 10^{14} / \text{cm}^3$ , preferably less than  $1 \times 10^{12} / \text{cm}^3$ , and more preferably less than  $1 \times 10^{11} / \text{cm}^3$  or less than  $6.0 \times 10^{10} / \text{cm}^3$ .

[0093]

25 A transistor using a highly-purified oxide semiconductor is characterized in that the off-current is much lower than that of a transistor including a semiconductor containing silicon, for example.

[0094]

The following shows the result of measuring the off-current characteristics of a  
30 transistor with an evaluation element (also referred to as TEG: Test Element Group). Note that the description is made here on an n-channel transistor.

[0095]

In the TEG, a transistor with  $L/W = 3 \mu\text{m}/10000 \mu\text{m}$ , which includes 200 transistors with  $L/W = 3 \mu\text{m}/50 \mu\text{m}$  (thickness  $d$ : 30 nm) connected in parallel, is provided. FIG. 28 illustrates the initial characteristics of the transistor. Here,  $V_G$  is in the range of  $-20 \text{ V}$  to  $+5 \text{ V}$  inclusive. In order to measure the initial characteristics of the transistor, the characteristics of changes in the source-drain current (hereinafter, referred to as a drain current or  $I_D$ ), i.e.,  $V_G$ - $I_D$  characteristics, were measured under the conditions where the substrate temperature was set to room temperature, the voltage between the source and the drain (hereinafter, referred to as a drain voltage or  $V_D$ ) was set to 10 V, and the voltage between the source and the gate (hereinafter, referred to as a gate voltage or  $V_G$ ) was changed from  $-20 \text{ V}$  to  $+20 \text{ V}$ .

[0096]

As illustrated in FIG. 28, the transistor with a channel width  $W$  of 10000  $\mu\text{m}$  has an off-current of  $1 \times 10^{-13} \text{ A}$  or less at  $V_D$  of 1 V and 10 V, which is less than or equal to the resolution (100 fA) of a measurement device (a semiconductor parameter analyzer, Agilent 4156C manufactured by Agilent Technologies Inc.). The off-current per micrometer of the channel width corresponds to 10 aA/ $\mu\text{m}$ .

[0097]

Note that in this specification, the off-current (also referred to as leakage current) means a current flowing between a source and a drain of an n-channel transistor when a predetermined gate voltage in the range of  $-20 \text{ V}$  to  $-5 \text{ V}$  inclusive is applied at room temperature in the case where the n-channel transistor has a positive threshold voltage  $V_{th}$ . Note that the room temperature is  $15 \text{ }^\circ\text{C}$  to  $25 \text{ }^\circ\text{C}$  inclusive. A transistor including an oxide semiconductor that is disclosed in this specification has a current per unit channel width ( $W$ ) of 100 aA/ $\mu\text{m}$  or less, preferably 1 aA/ $\mu\text{m}$  or less, and more preferably 10 zA/ $\mu\text{m}$  or less at room temperature.

[0098]

Moreover, the transistor including a high-purity oxide semiconductor has favorable temperature characteristics. Typically, in the temperature range of  $-25 \text{ }^\circ\text{C}$  to  $150 \text{ }^\circ\text{C}$  inclusive, the current-voltage characteristics of the transistor, such as on-current, off-current, field-effect mobility, S value, and threshold voltage, hardly change and

deteriorate due to temperature. In addition, a high-purity oxide semiconductor hardly deteriorates due to light irradiation, which results in an increase in the reliability of, in particular, a semiconductor device using light such as a photosensor.

[0099]

5           Although the bottom-gate transistor 1004 is shown as an example in this embodiment, a top-gate transistor 2004 may be used as illustrated in FIG. 20. The transistor 2004 includes a semiconductor film 2012, an electrode 2013, an electrode 2014, a gate insulating film 2011, and a gate electrode 2010.

[0100]

10           Note that for each of the aforementioned semiconductor elements, a thin film semiconductor or a bulk semiconductor can be used.

[0101]

This embodiment can be implemented in appropriate combination with the other embodiments.

15 [0102]

(Embodiment 4)

This embodiment shows an example of the circuit, layout, and cross-sectional structure of the semiconductor device, which is different from the example shown in Embodiments 1 to 3.

20 [0103]

FIG. 21 is an example of the circuit diagram of the photosensor, in which the transistor 101 and the transistor 102 of FIG. 1 are omitted.

[0104]

25           FIG. 22 is an example of the layout of the circuit in FIG. 21. The light-receiving area can be increased because of a reduction in the number of elements.

[0105]

30           Since the transistor 103 and the transistor 104 are formed to overlap with the n-region 201 of the PIN photodiode 100 in FIG. 22, the light-receiving area can be increased. The light-receiving area can also be increased when the wiring 109 or the like is made of a light-transmitting material and is formed to overlap with the i-region 202.

[0106]

FIG. 23 is an example of the cross-sectional view of the photosensor in FIG. 21. A transistor 3001 corresponds to the transistor 103 in FIG. 21, and is formed to overlap with the n-region 1023 of the photodiode 1002. The transistor 3001, which is a bottom-gate thin film transistor, includes a gate electrode 3010, a gate insulating film 3011, a semiconductor film 3012, an electrode 3013, and an electrode 3014. The transistor 3001 may be a top-gate transistor like the transistor 2004 illustrated in FIG. 20.

[0107]

For the semiconductor film 3012, an amorphous semiconductor, a microcrystal semiconductor, a polycrystalline semiconductor, an oxide semiconductor, a single crystal semiconductor, or the like can be used. In particular, an electric signal can be amplified with high accuracy by using an oxide semiconductor to obtain a transistor with an extremely low off-current.

[0108]

It is preferable that the transistor 104 in FIG. 21 be also formed over the same surface and in the same process as the transistor 103 so as to overlap with the n-region 1023. When an oxide semiconductor is used for the transistor 104, an output signal can be read with high accuracy.

[0109]

This embodiment can be implemented in appropriate combination with the other embodiments.

[0110]

(Embodiment 5)

This embodiment shows an example of the circuit of the semiconductor device, which is different from the example shown in the above embodiments.

[0111]

FIGS. 27A to 27F are examples of the circuit of the photosensor. The layout or structure disclosed in this specification, which leads to an increase in receiving area, can be applied to these circuits.

[0112]

This embodiment can be implemented in appropriate combination with the other embodiments.

[0113]

(Embodiment 6)

This embodiment shows an example of the cross-sectional structure of the semiconductor device, which is different from the example shown in Embodiment 3.

5 [0114]

FIG. 24 is an example of the cross-sectional view of the photosensor.

[0115]

In FIG. 24, a transistor 4001 is formed over the substrate 1001 having an insulating surface, and a photodiode 4002 is formed over the transistor 4001.

10 [0116]

The transistor 4001 can be applied to all the transistors illustrated in FIG. 1, FIG. 7, FIG. 13, FIG. 21, and FIGS. 27A to 27F. That is, all the transistors can be formed of the same material, over the same surface, and in the same process, resulting in cost reduction.

15 [0117]

The photodiode 4002 is a vertical-junction PIN photodiode, in which an n-region 4003, an i-region 4004, and a p-region 4005 are stacked. Note that the order of stacking can be a p-region, an i-region, and an n-region. The photodiode can be a PN photodiode without the i-region 4004. For the photodiode 4002, an amorphous semiconductor, a microcrystal semiconductor, a polycrystalline semiconductor, an oxide semiconductor, a single crystal semiconductor, or the like can be used. In particular, silicon semiconductor is preferably used because the quantum efficiency of the photodiode 4002 can be improved.

20 [0118]

The light 1202 emitted from the object to be detected 1201 enters the photodiode 4002. With such a structure, there is no element blocking the light 1202 entering the photodiode 4002; thus, a light-receiving area can be made as large as possible.

[0119]

30 For the transistor 4001, an amorphous semiconductor, a microcrystal semiconductor, a polycrystalline semiconductor, an oxide semiconductor, a single crystal semiconductor, or the like can be used. In particular, light can be detected with

high accuracy by using an oxide semiconductor to obtain a transistor with an extremely low off-current.

[0120]

Note that some of the transistors included in the photosensor can be formed  
5 over a surface different from that over which the transistor 4001 is formed. The other structure is the same as that shown in Embodiment 3.

[0121]

This embodiment can be implemented in appropriate combination with the other embodiments.

10 [0122]

(Embodiment 7)

In this embodiment, an example of a semiconductor device using the photosensor will be described.

[0123]

15 FIG. 25 is an example of an image sensor provided with the photosensor. The image sensor includes a photosensor portion 5001 and a photosensor control circuit 5002. The photosensor portion 5001 includes a plurality of photosensors 5003 arranged in matrix. The photosensor control circuit 5002 includes a photosensor reading circuit 5004 and a photosensor driver circuit 5005. The area sensor is shown  
20 here, though a line sensor can also be used. The image sensor is applied to a digital still camera, a cellular phone, and the like.

[0124]

The photosensor shown in the other embodiments can be applied to the photosensors 5003.

25 [0125]

FIG. 26 is an example of a display device provided with the photosensor. A display panel 6000 includes a pixel portion 6001, a display element control circuit 6002, and a photosensor control circuit 6003. The pixel portion 6001 includes pixels 6004 arranged in matrix, each of which includes a display element 6005 and a photosensor  
30 6006. The display element control circuit 6002 includes display element driver circuits 6007 and 6008. The photosensor control circuit 6003 includes a photosensor reading circuit 6009 and a photosensor driver circuit 6010. The display device is

applied to a touch panel and the like.

[0126]

The photosensor shown in the other embodiments can be applied to the photosensor 6006.

5 [0127]

Note that the photosensor 6006 can be provided outside the pixel 6004.

[0128]

As the display element 6005, a liquid crystal element, an EL element, an electrophoretic element, or the like can be used.

10 [0129]

The display element control circuit 6002 and the photosensor control circuit 6003 can be provided outside the display panel 6000.

[0130]

15 This embodiment can be implemented in appropriate combination with the other embodiments.

[0131]

(Embodiment 8)

In this embodiment, the operation of the semiconductor device will be described.

20 [0132]

The operation of the circuit in FIG. 1 will be described as an example of the operation of the photosensor. FIG. 29 is an example of a timing chart of the circuit in FIG. 1.

[0133]

25 In FIG. 29, signals 301 to 305 respectively show the potentials of the wiring 107, the wiring 106, the wiring 109, the wiring 105, and the wiring 110 in FIG. 1. Here, the wiring 120 is assumed to be at the ground potential.

[0134]

30 At time A, the signal 301 is set to "H (High)" and the signal 302 is set to "H" (reset operation starts), and then, the photodiode 100 is turned on and the signal 304 is set to "H".

[0135]

At time B, the signal 301 is set to “L (Low)” and the signal 302 is kept at “H” (the reset operation is completed and accumulation operation starts). Then, the signal 304 starts to be lowered due to the off-current of the photodiode 100. Since the off-current of the photodiode 100 increases when light enters, the signal 304 changes depending on the amount of incident light.

[0136]

At time C, the signal 302 is set to “L” (the accumulation operation is completed), whereby the signal 304 becomes constant. Here, the signal 304 is determined by the charge that has been supplied to the wiring 105 from the photodiode 100 during the accumulation operation. That is, the charge accumulated in the gate of the transistor 103 changes depending on the light entering the photodiode 100.

[0137]

At time D, the signal 303 is set to “H” (selection operation starts). Then, the transistor 104 is turned on, and electrical conduction is established between the wiring 108 and the wiring 110 through the transistor 103 and the transistor 104. Thus, the signal 305 starts to be lowered. The rate at which the signal 305 is lowered depends on the current between the source and the drain of the transistor 103, namely, the amount of light that has emitted to the photodiode 100 during the accumulation operation.

[0138]

At time E, the signal 303 is set to “L” (the selection operation is completed), whereby the transistor 104 is turned off and the signal 305 becomes constant. The constant value of the signal 305 changes depending on the amount of light emitted to the photodiode 100. Consequently, the amount of light entering the photodiode 100 during the accumulation operation can be found by obtaining the potential of the signal 305.

[0139]

This embodiment can be implemented in appropriate combination with the other embodiments.

This application is based on Japanese Patent Application serial no. 2010-034173 filed with Japan Patent Office on February 19, 2010, the entire contents of

which are hereby incorporated by reference.

## CLAIMS

1. A semiconductor device comprising:

a light-receiving element configured to convert light into an electric signal;

a first transistor configured to transfer the electric signal; and

5 a second transistor configured to amplify the transferred electric signal,  
wherein the first transistor includes an oxide semiconductor.

2. A semiconductor device according to claim 1, further comprising an  
insulating film interposed between the first transistor and the light-receiving element.

10

3. A semiconductor device comprising:

a photodiode including a semiconductor film formed on a surface, the  
semiconductor film comprising an n-region and a p-region each in contact with the  
surface;

15 a first transistor configured to transfer an electric signal generated by the  
photodiode; and

a second transistor configured to amplify the transferred electric signal,

an insulating film interposed between the first transistor and the second  
transistor and between the first transistor and the photodiode;

20 wherein the first transistor includes an oxide semiconductor and overlaps one  
of the n-region and the p-region of the photodiode when seen from above.

4. A semiconductor device according to claim 1, wherein the light-receiving  
element and a channel formation region of the second transistor are made from a same  
25 semiconductor layer.

5. A semiconductor device according to claim 3, wherein the n-region, the  
p-region and a channel formation region of the second transistor are made from the  
same semiconductor layer.

30

6. A semiconductor device according to claim 1, wherein the light-receiving  
element and the second transistor include silicon.

7. A semiconductor device according to claim 3, wherein the photodiode and the second transistor include silicon.

5           8. A semiconductor device according to claim 1, further comprising a third transistor configured to control reading of an output signal generated by the second transistor.

10           9. A semiconductor device according to claim 3, further comprising a third transistor configured to control reading of an output signal generated by the second transistor.

15           10. A semiconductor device according to claim 1, further comprising a fourth transistor configured to control reset of a gate potential of the second transistor;  
              wherein a channel formation region of the fourth transistor is made from a same layer as a channel formation region of the first transistor.

20           11. A semiconductor device according to claim 3, further comprising a fourth transistor configured to control reset of a gate potential of the second transistor;  
              wherein a channel formation region of the fourth transistor is made from a same layer as a channel formation region of the first transistor.

25           12. A semiconductor device according to claim 1, further comprising a wiring formed over a light-receiving portion of the light-receiving element,  
              wherein the wiring includes a light-transmitting material.

30           13. A semiconductor device according to claim 3, further comprising a wiring formed over the photodiode,  
              wherein the wiring includes a light-transmitting material.

14. A semiconductor device comprising:  
a light-receiving element configured to convert light into an output electric

signal;

a first transistor, one of a source and a drain of the first transistor being electrically connected to an output terminal of the light-receiving element; and

5 a second transistor, a gate of the second transistor being electrically connected to the other of the source and the drain of the first transistor,  
wherein the first transistor includes an oxide semiconductor.

15. A semiconductor device according to claim 14, further comprising an insulating film interposed between the first transistor and the light-receiving element.

10

16. A semiconductor device comprising:

a photodiode including a semiconductor film formed on a surface, the semiconductor film comprising an n-region and a p-region each in contact with the surface;

15 a first transistor, one of a source and a drain of the first transistor being electrically connected to an output terminal of the photodiode; and

a second transistor, a gate of the second transistor being electrically connected to the other of the source and the drain of the first transistor,

20 an insulating film interposed between the first transistor and the second transistor and between the first transistor and the photodiode;

wherein the first transistor includes an oxide semiconductor and overlaps one of the n-region and the p-region of the photodiode when seen from above.

25 17. A semiconductor device according to claim 14, wherein the light-receiving element and a channel formation region of the second transistor are made from a same semiconductor layer.

30 18. A semiconductor device according to claim 16, wherein the n-region, the p-region and a channel formation region of the second transistor are made from the same semiconductor layer.

19. A semiconductor device according to claim 14, wherein the light-receiving

element and the second transistor include silicon.

20. A semiconductor device according to claim 16, wherein the photodiode and the second transistor include silicon.

5

21. A semiconductor device according to claim 14, further comprising a third transistor, one of a source and a drain of the third transistor being electrically connected to one of a source and a drain of the second transistor.

10

22. A semiconductor device according to claim 16, further comprising a third transistor, one of a source and a drain of the third transistor being electrically connected to one of a source and a drain of the second transistor.

15

23. A semiconductor device according to claim 14, further comprising a fourth transistor, one of a source and a drain of the fourth transistor being electrically connected to a gate of the second transistor;

wherein a channel formation region of the fourth transistor is made from a same layer as a channel formation region of the first transistor.

20

24. A semiconductor device according to claim 16, further comprising a fourth transistor, one of a source and a drain of the fourth transistor being electrically connected to a gate of the second transistor;

wherein a channel formation region of the fourth transistor is made from a same layer as a channel formation region of the first transistor.

25

25. A semiconductor device according to claim 14, further comprising a wiring formed over a light-receiving portion of the light-receiving element,

wherein the wiring includes a light-transmitting material.

30

26. A semiconductor device according to claim 16, further comprising a wiring formed over a light-receiving portion of the photodiode,

wherein the wiring includes a light-transmitting material.

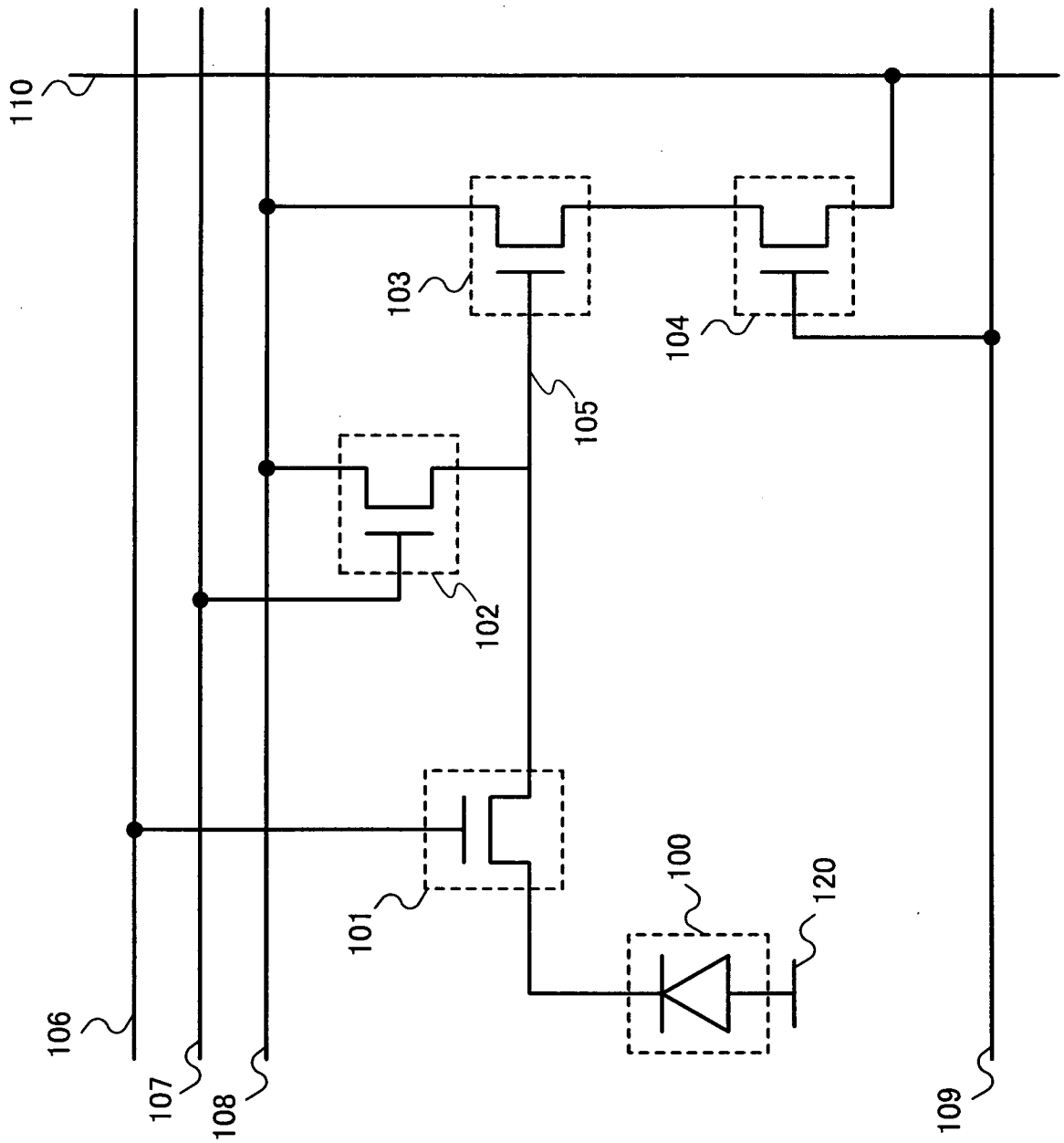


FIG. 1

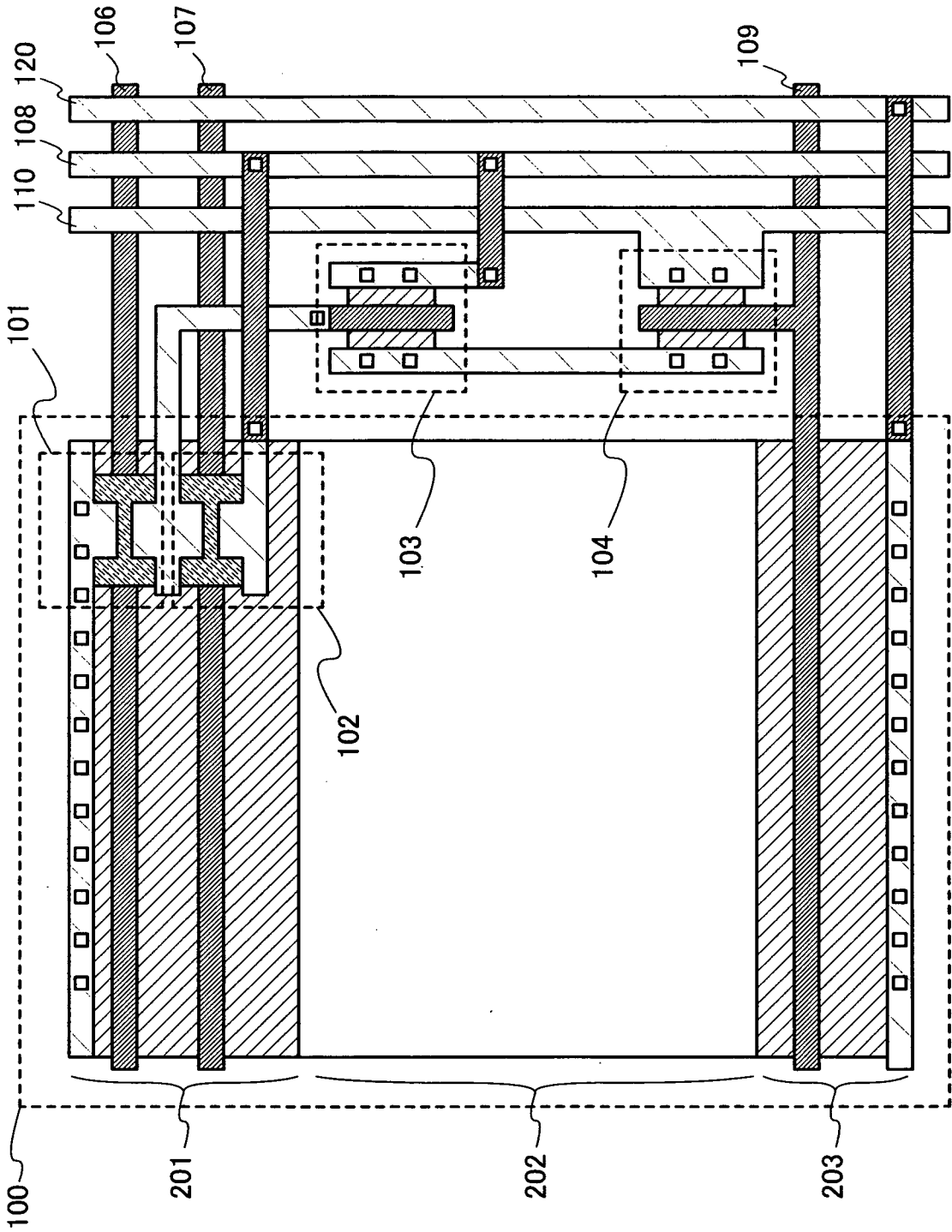


FIG. 2

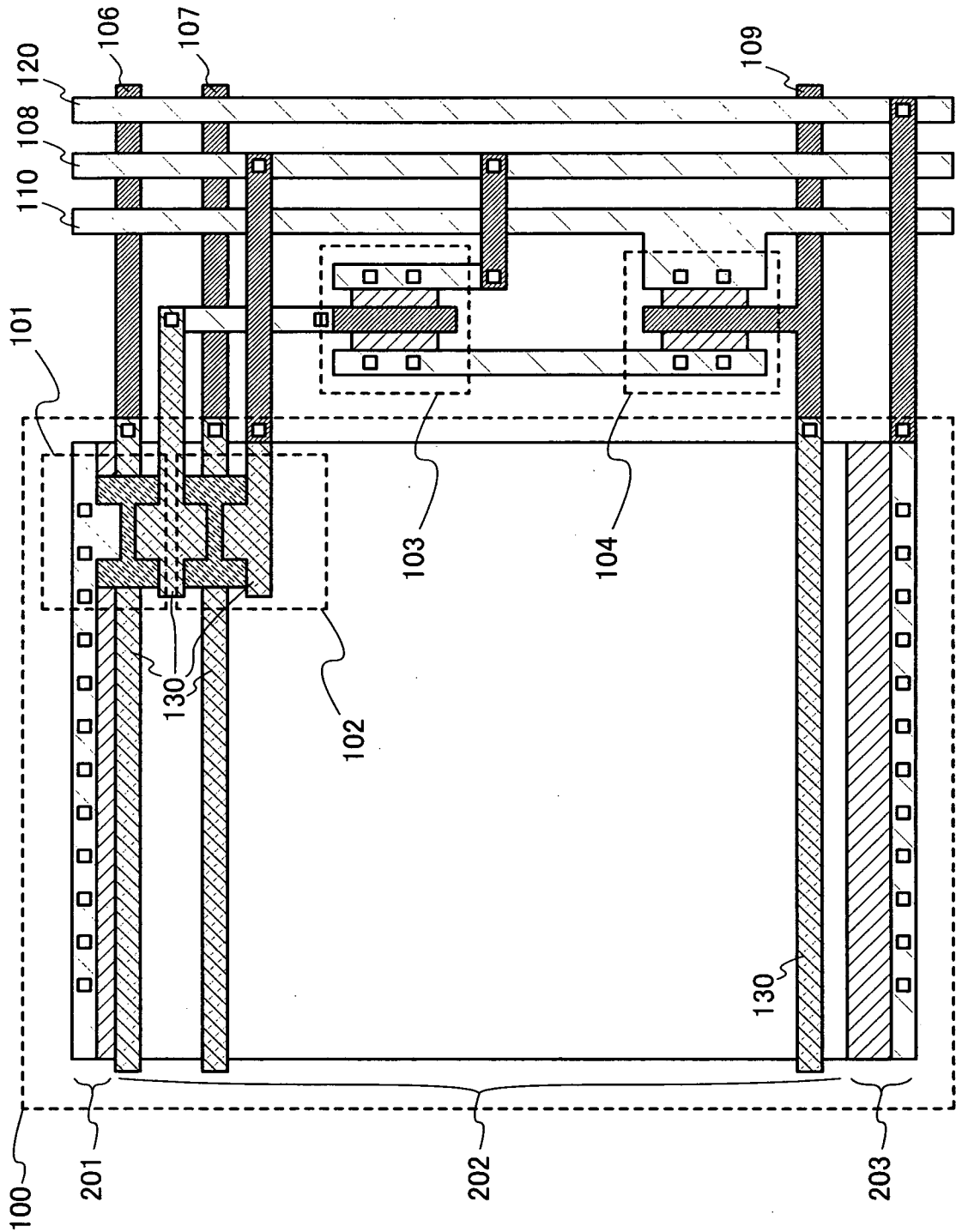


FIG. 3

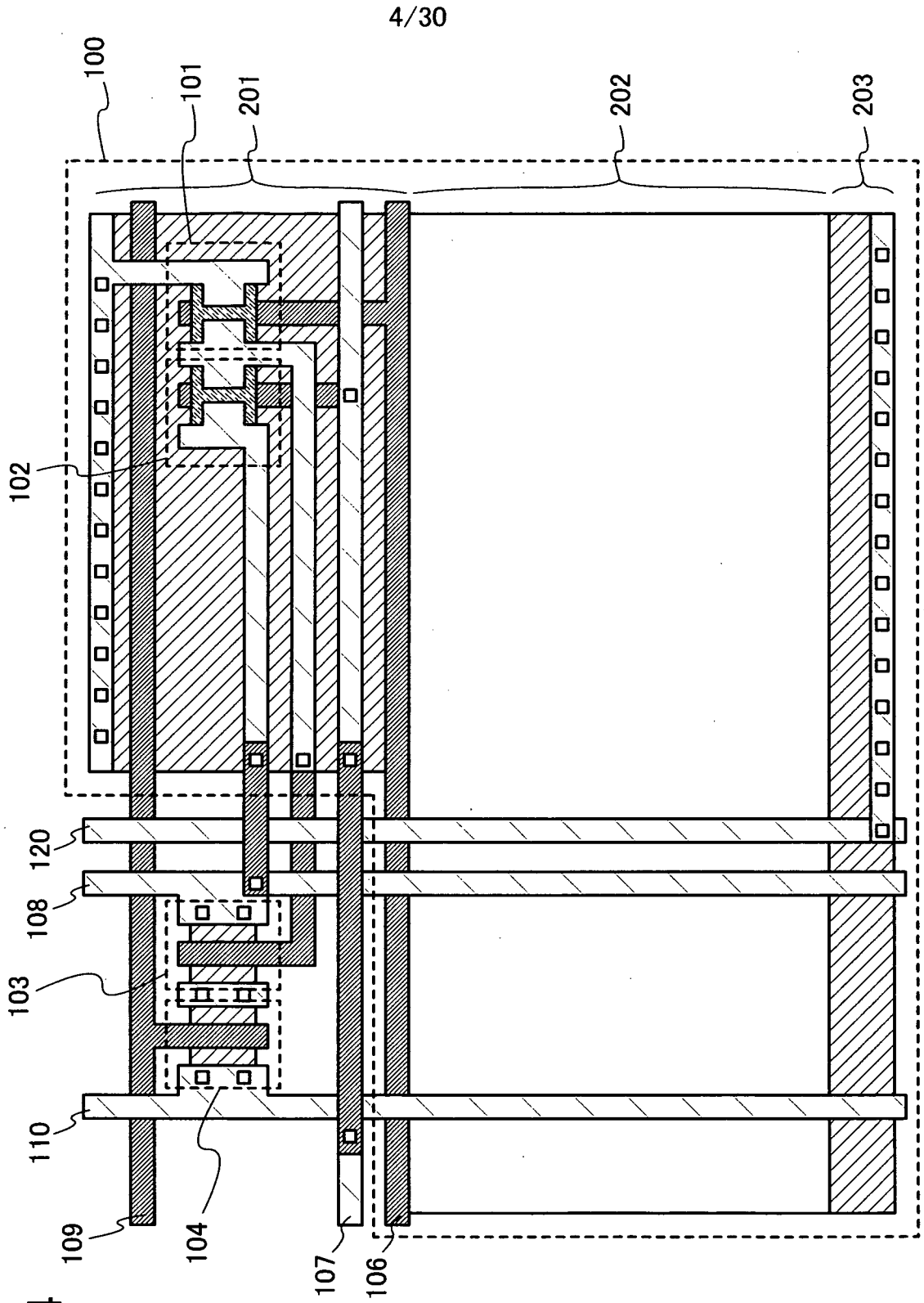


FIG. 4

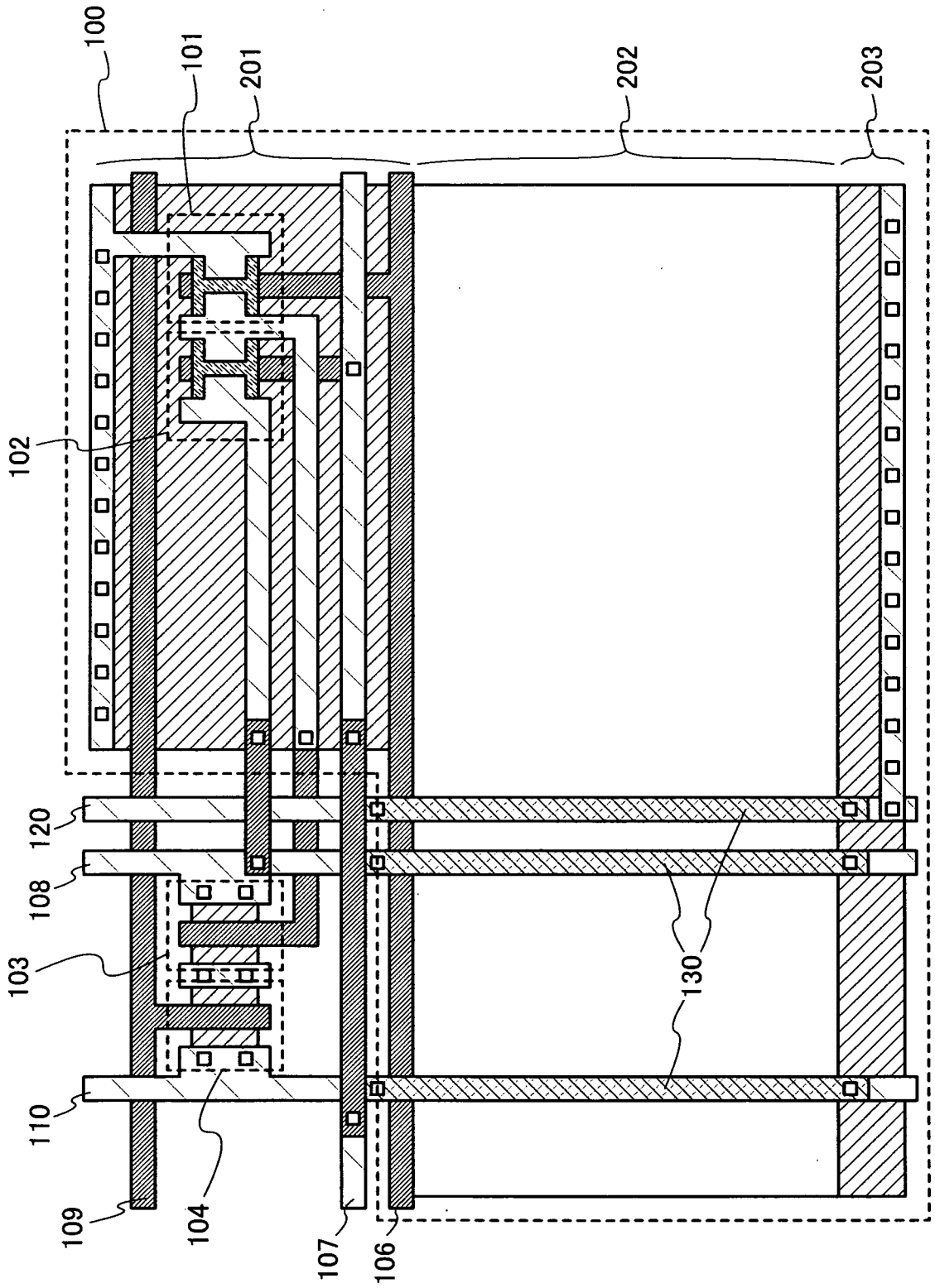


FIG. 5

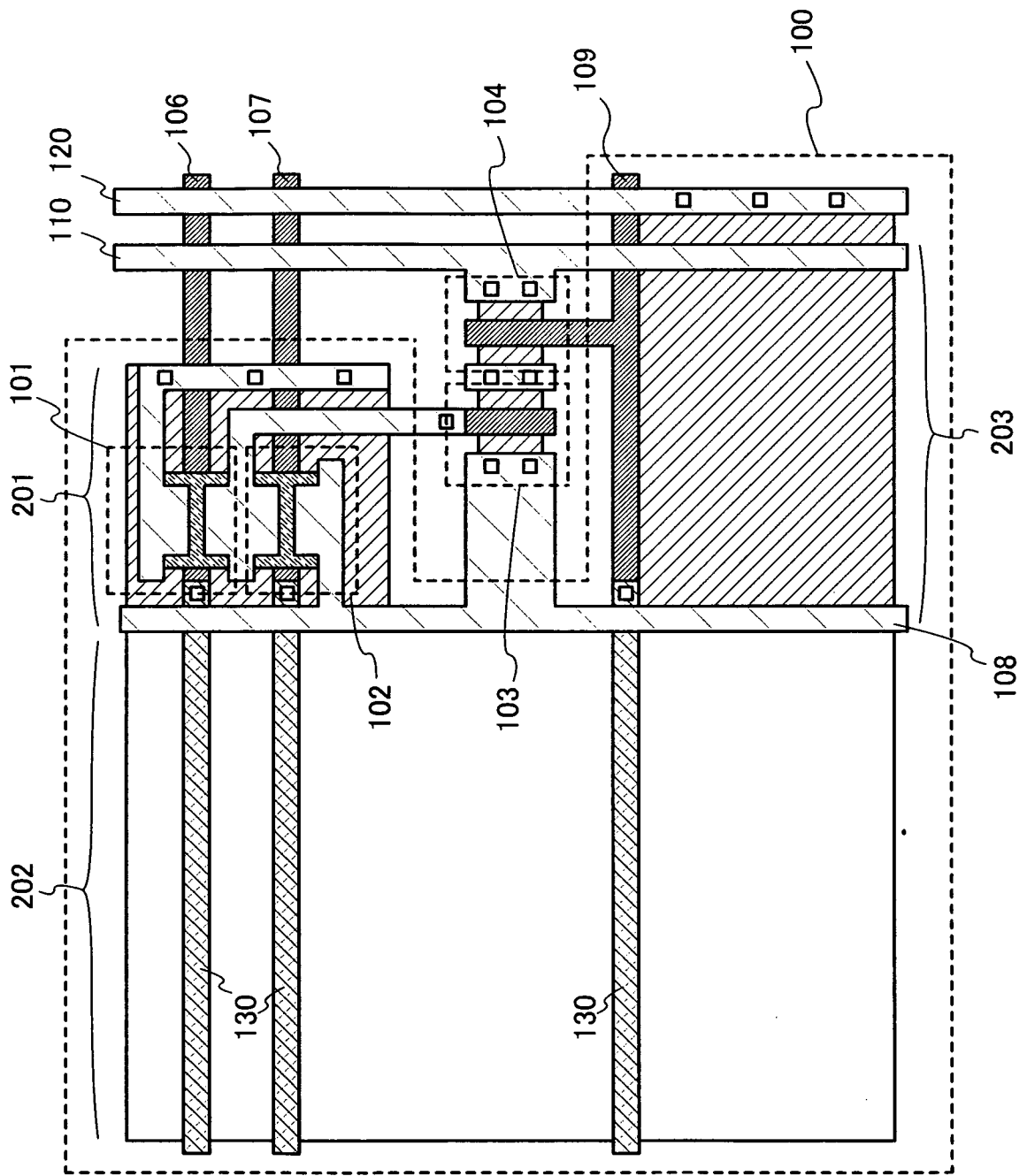


FIG. 6

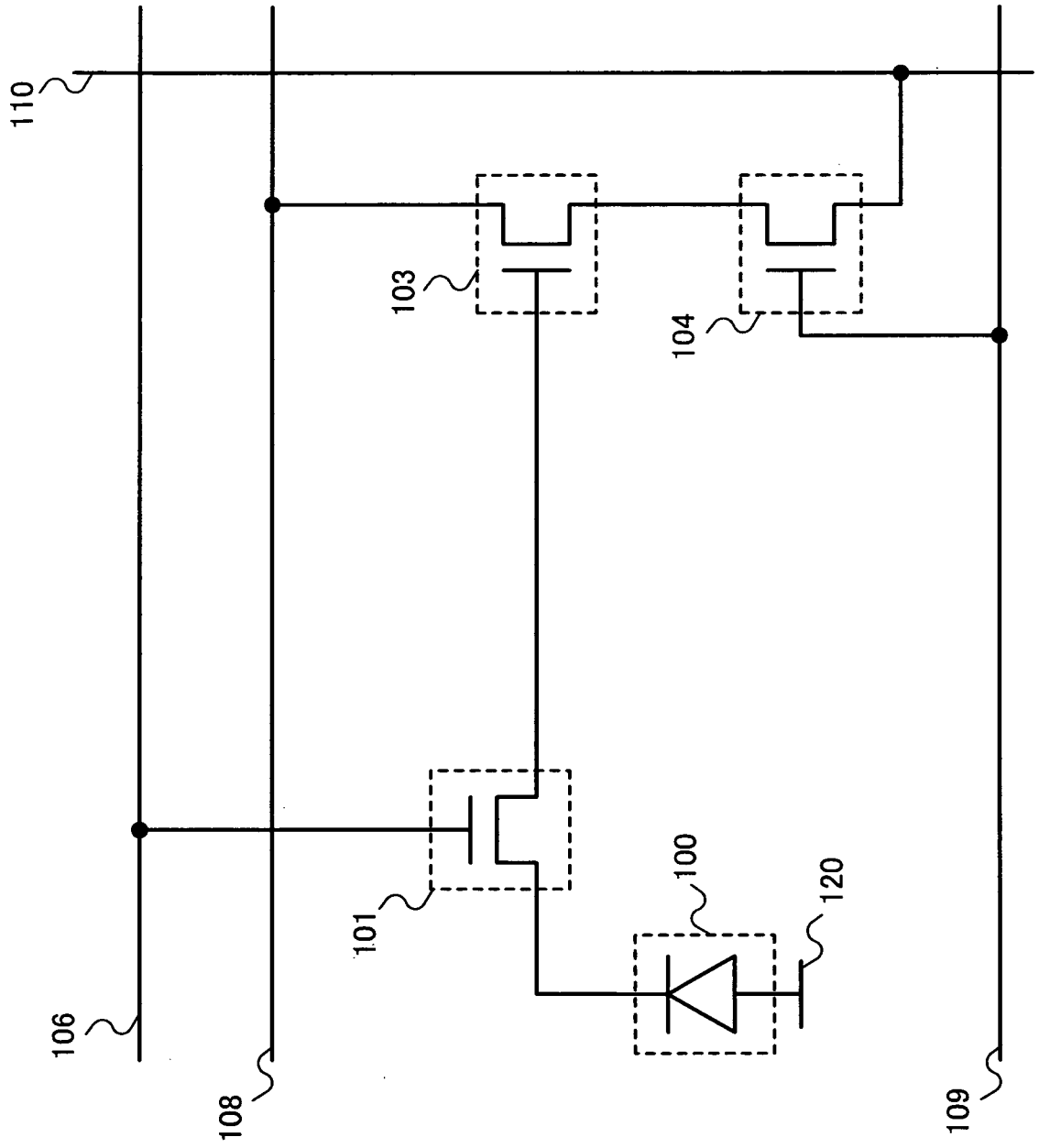


FIG. 7

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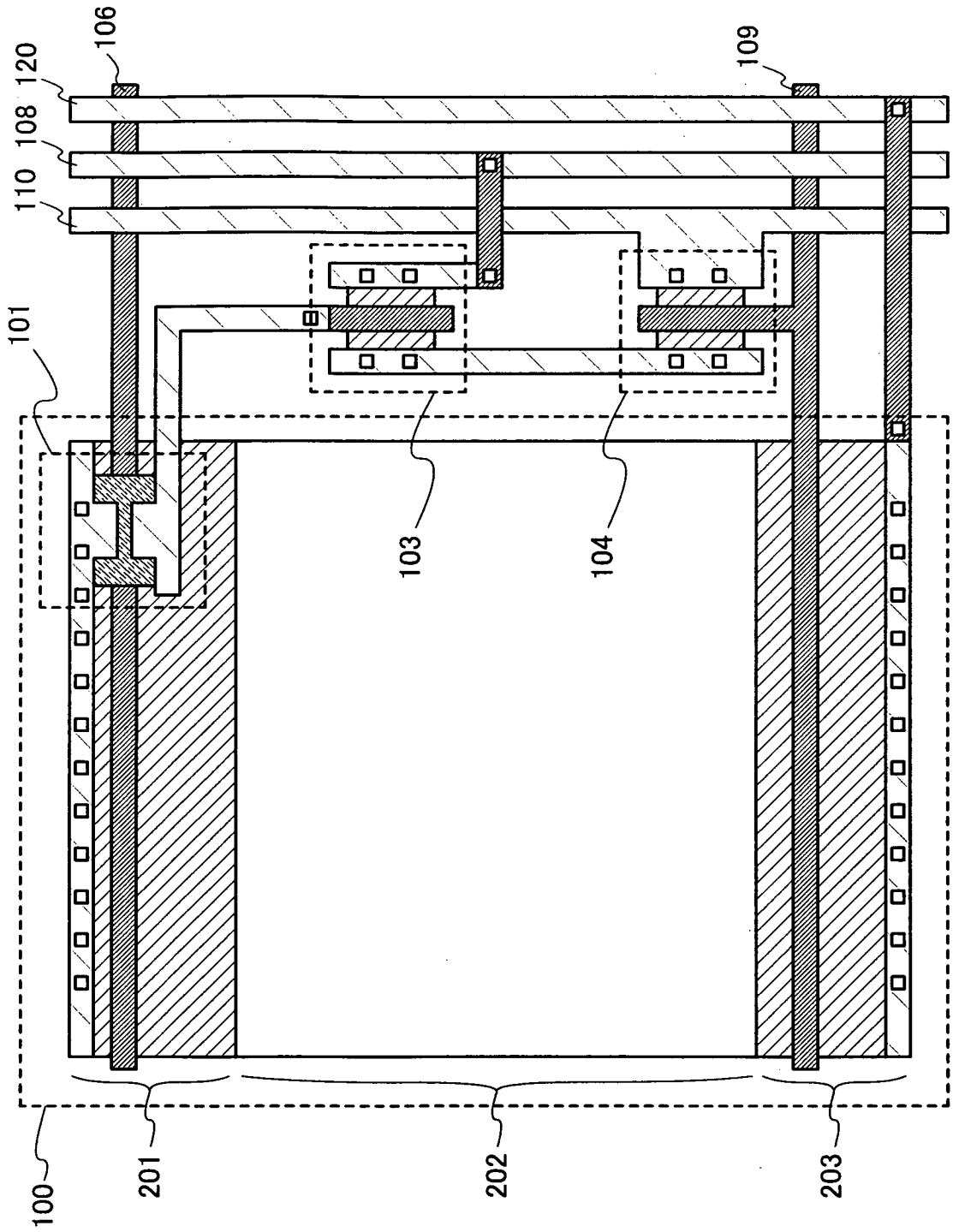


FIG. 8

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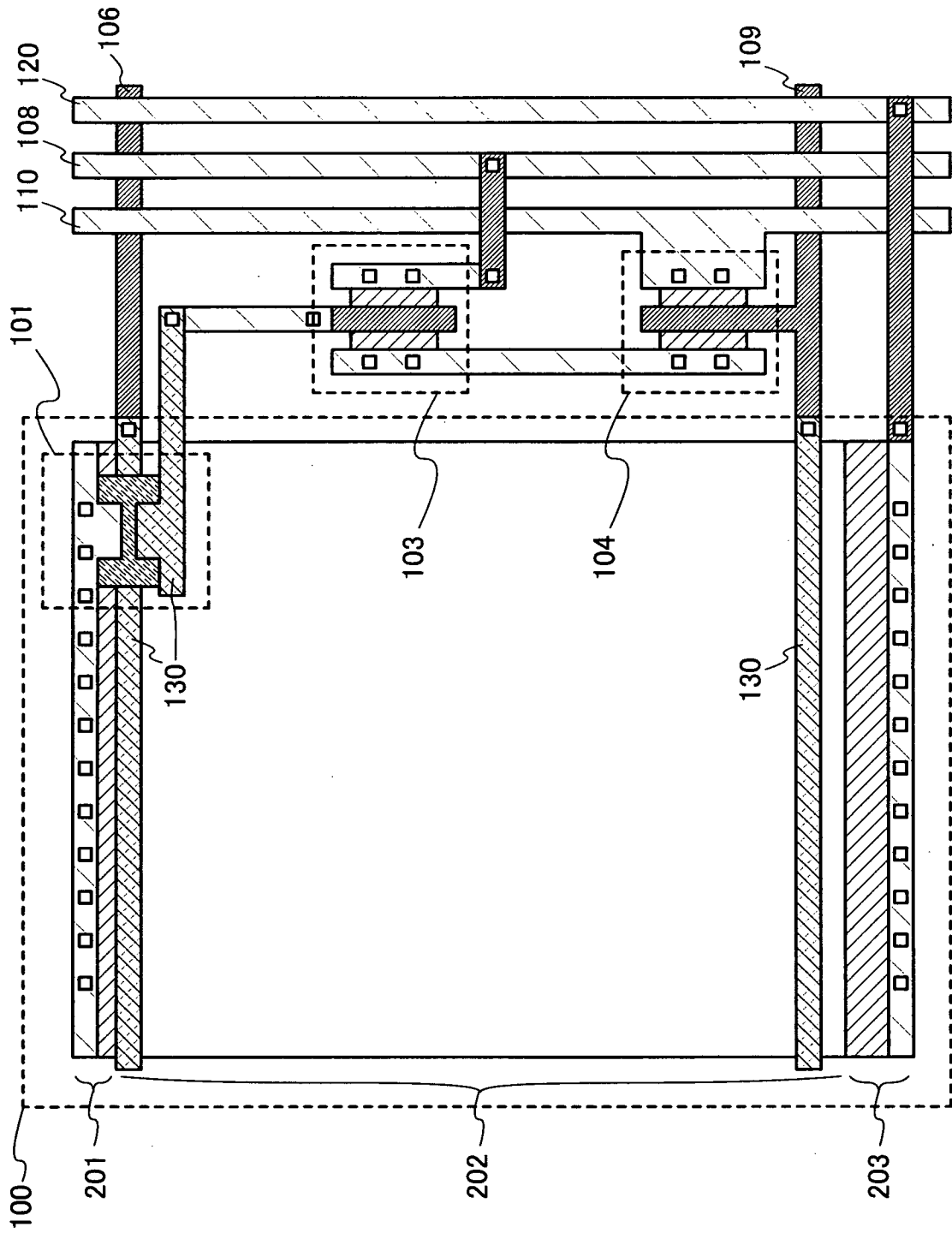


FIG. 9

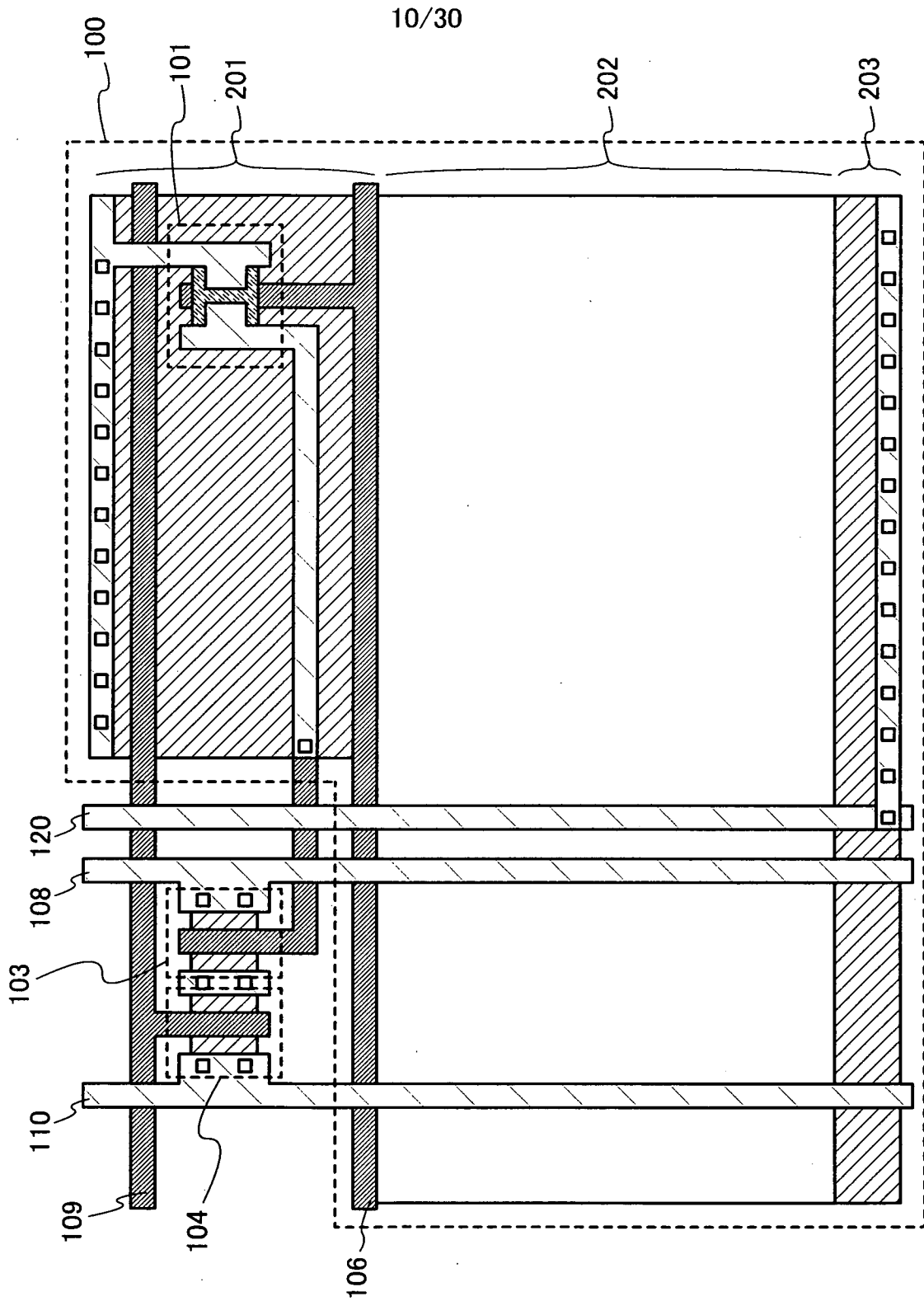


FIG. 10

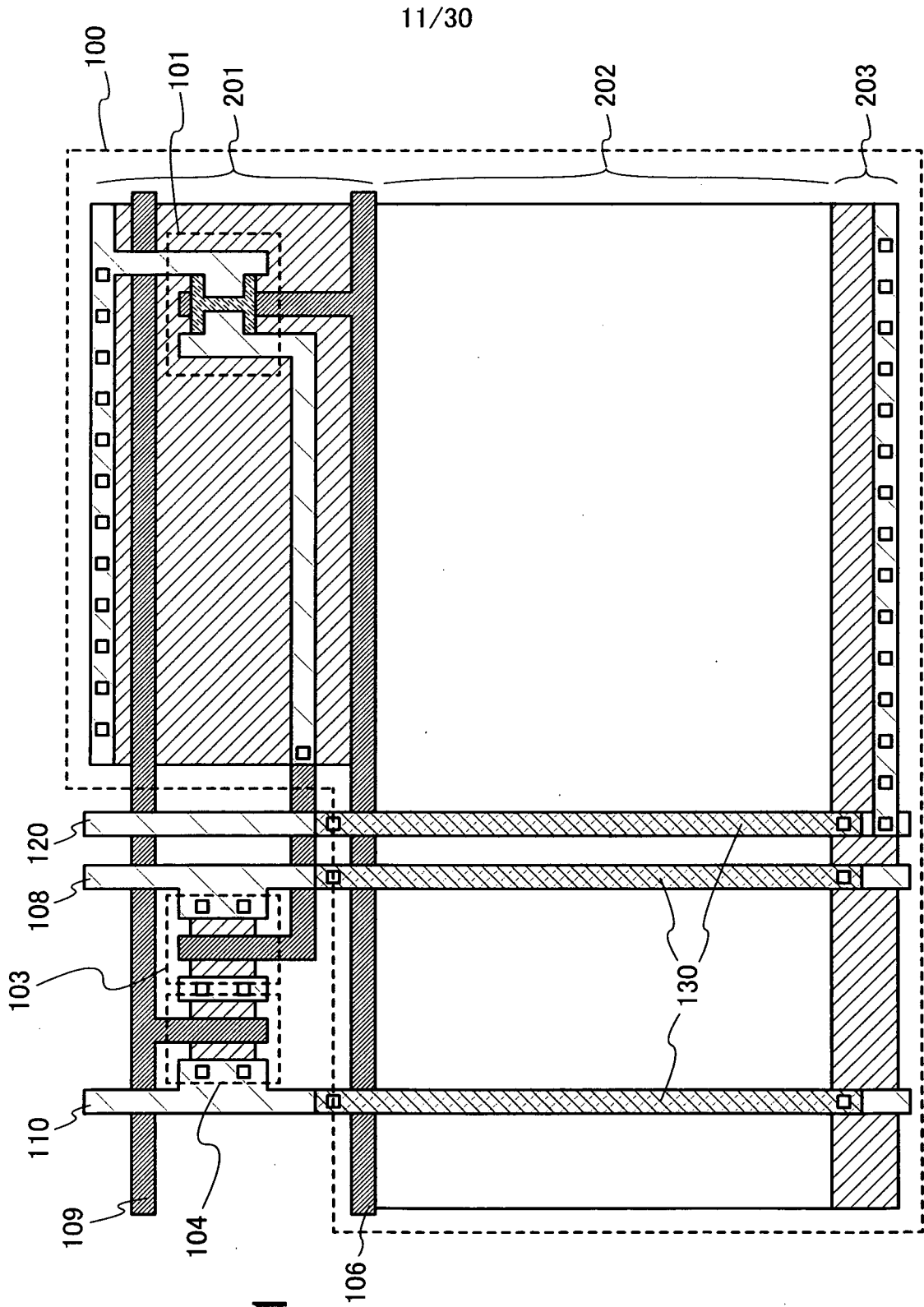


FIG. 11

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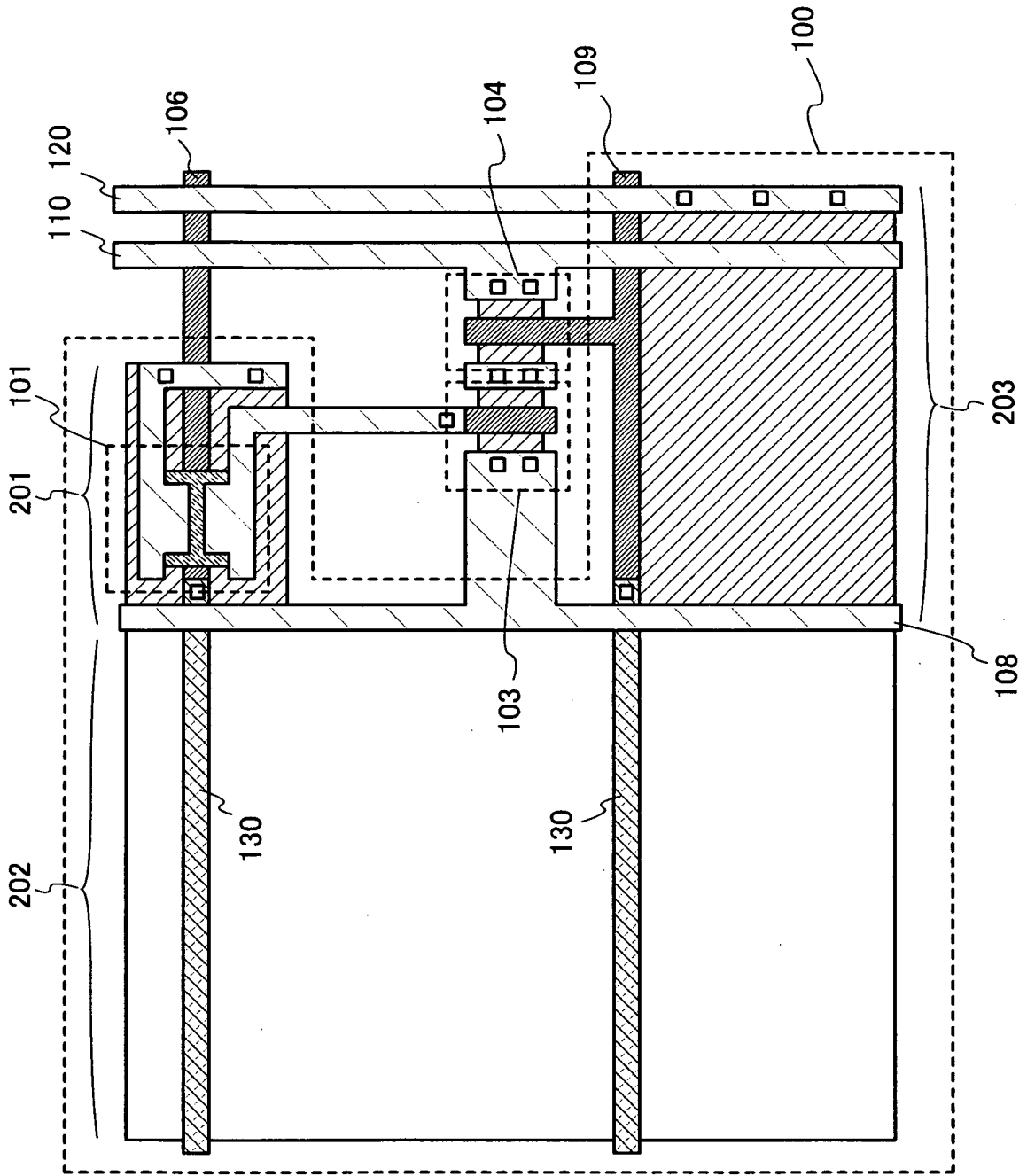


FIG. 12

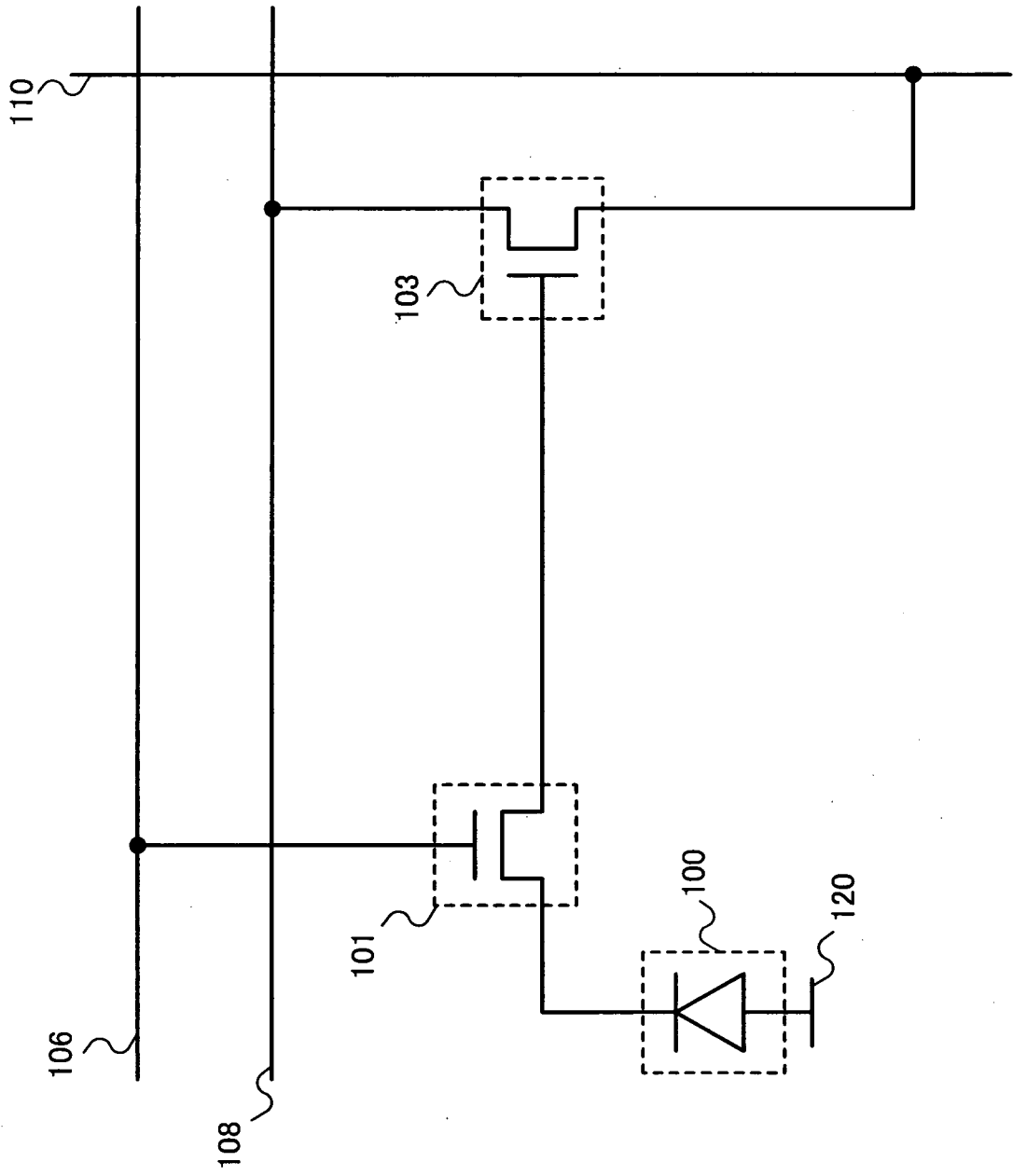


FIG. 13

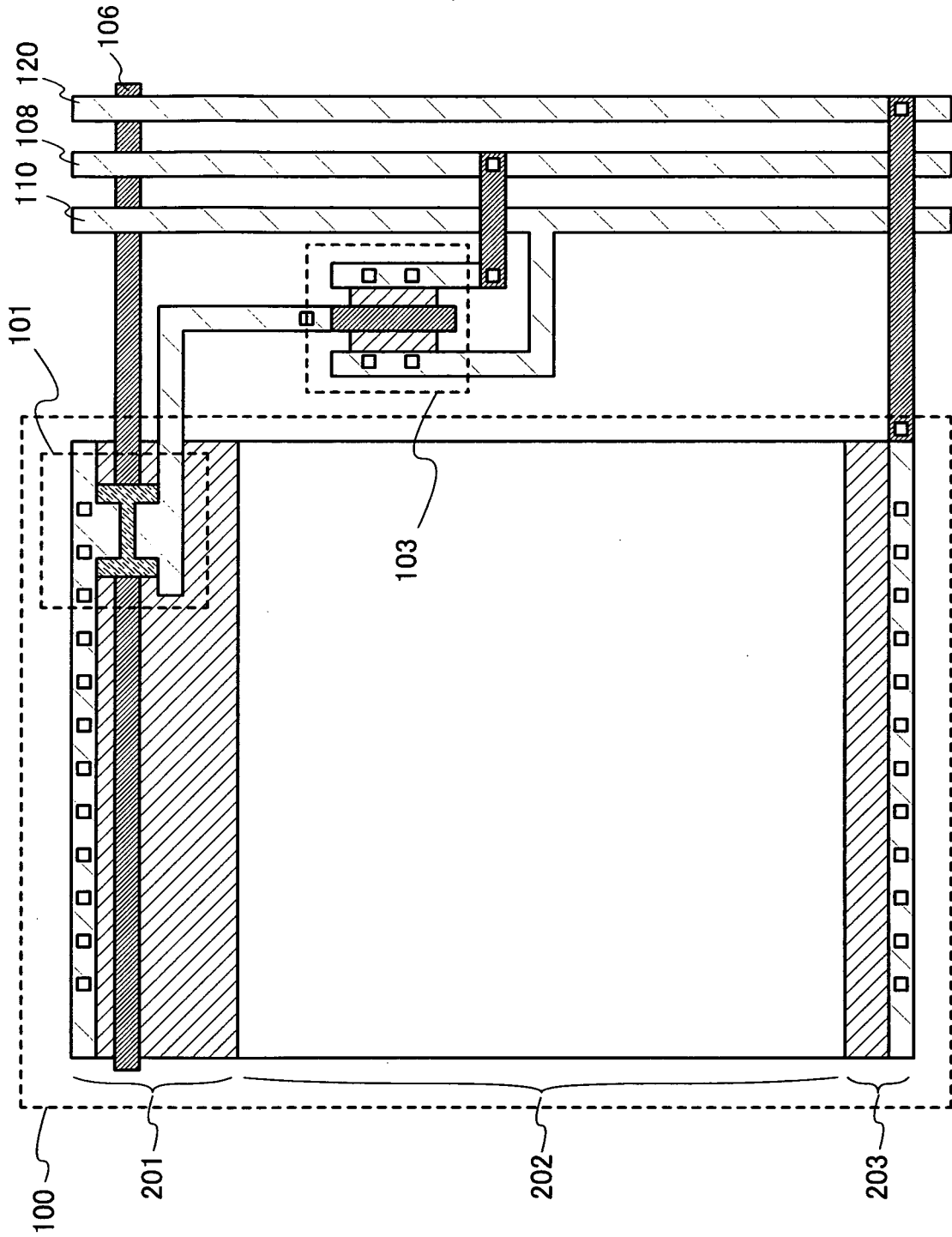


FIG. 14

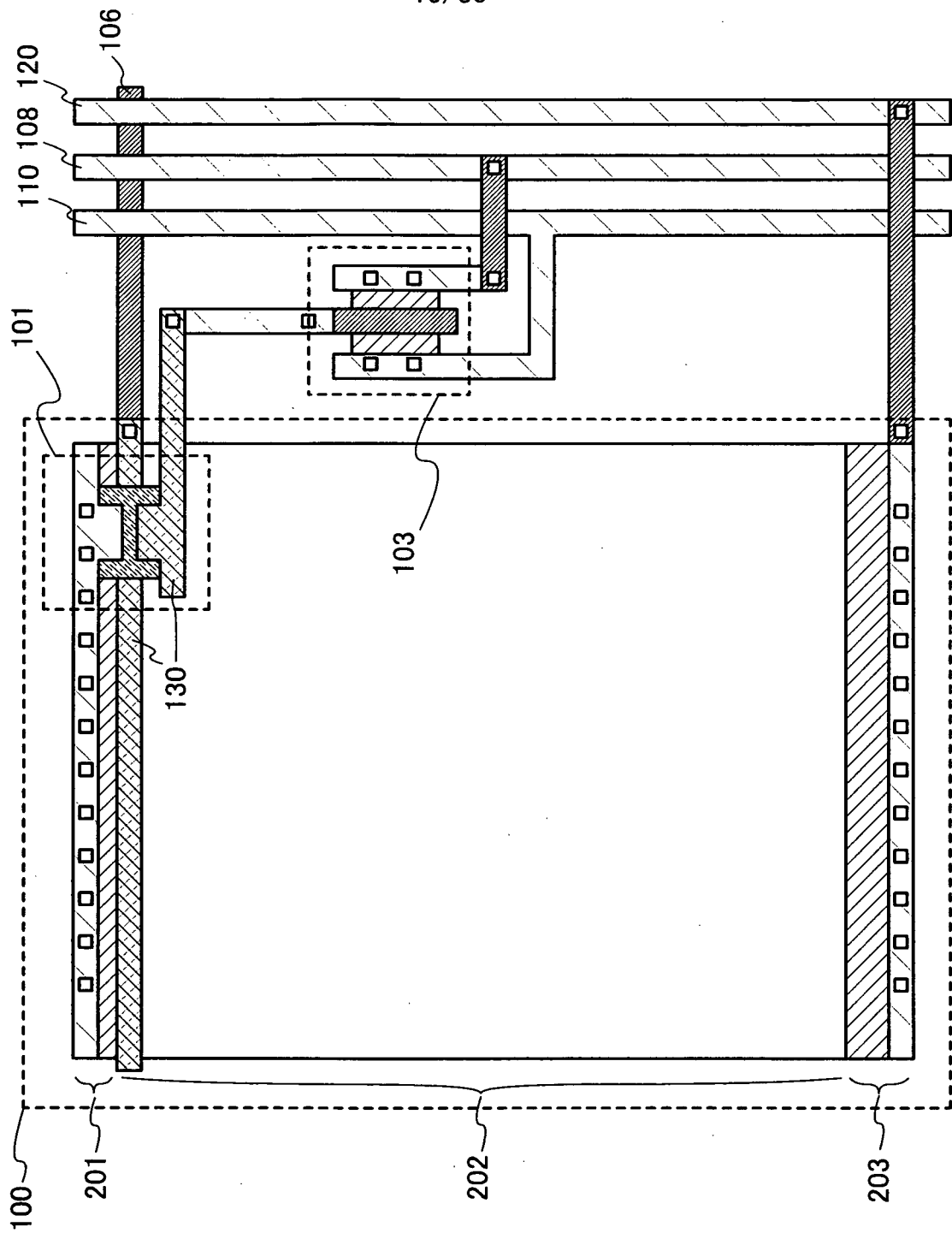


FIG. 15

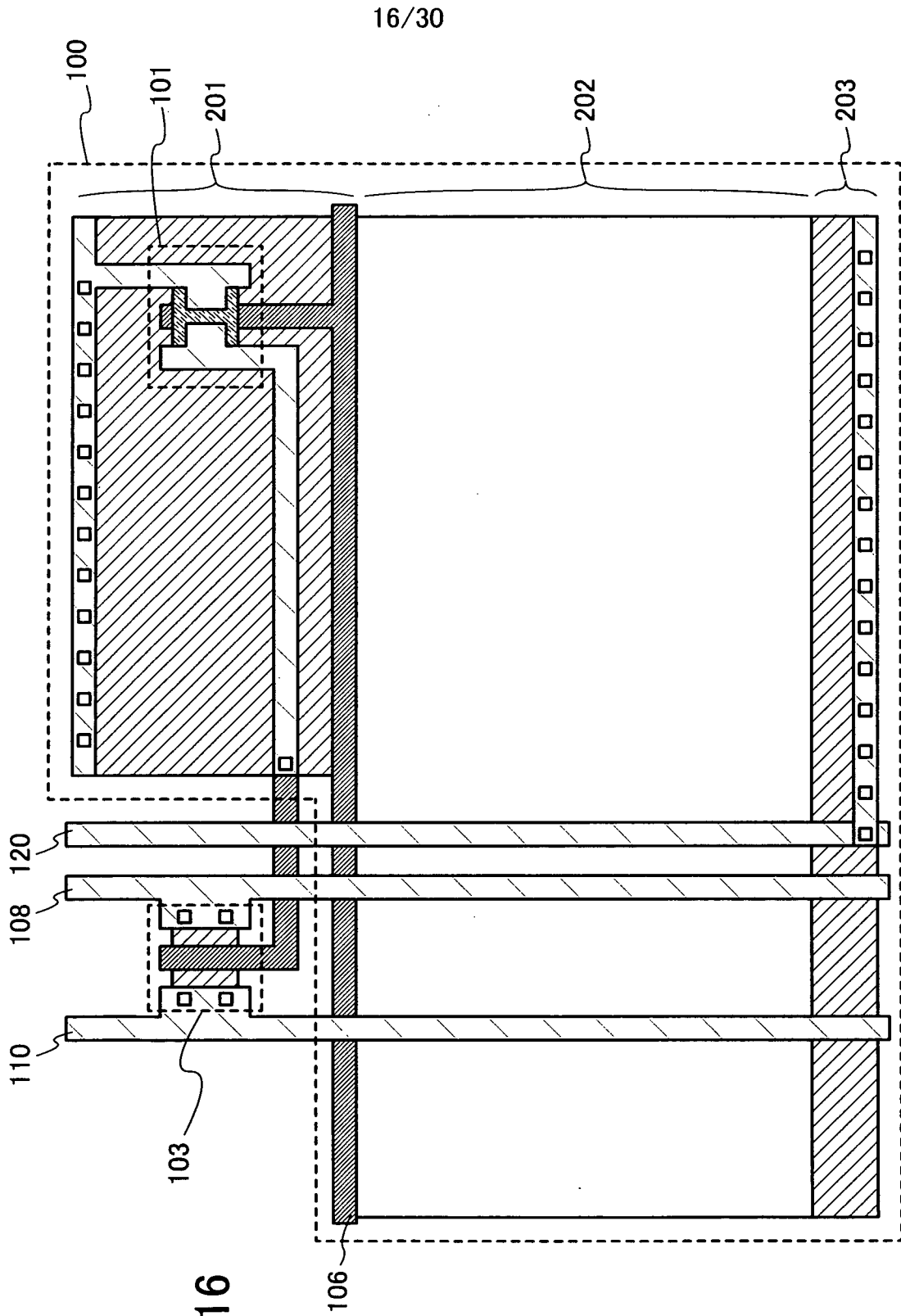


FIG. 16

17/30

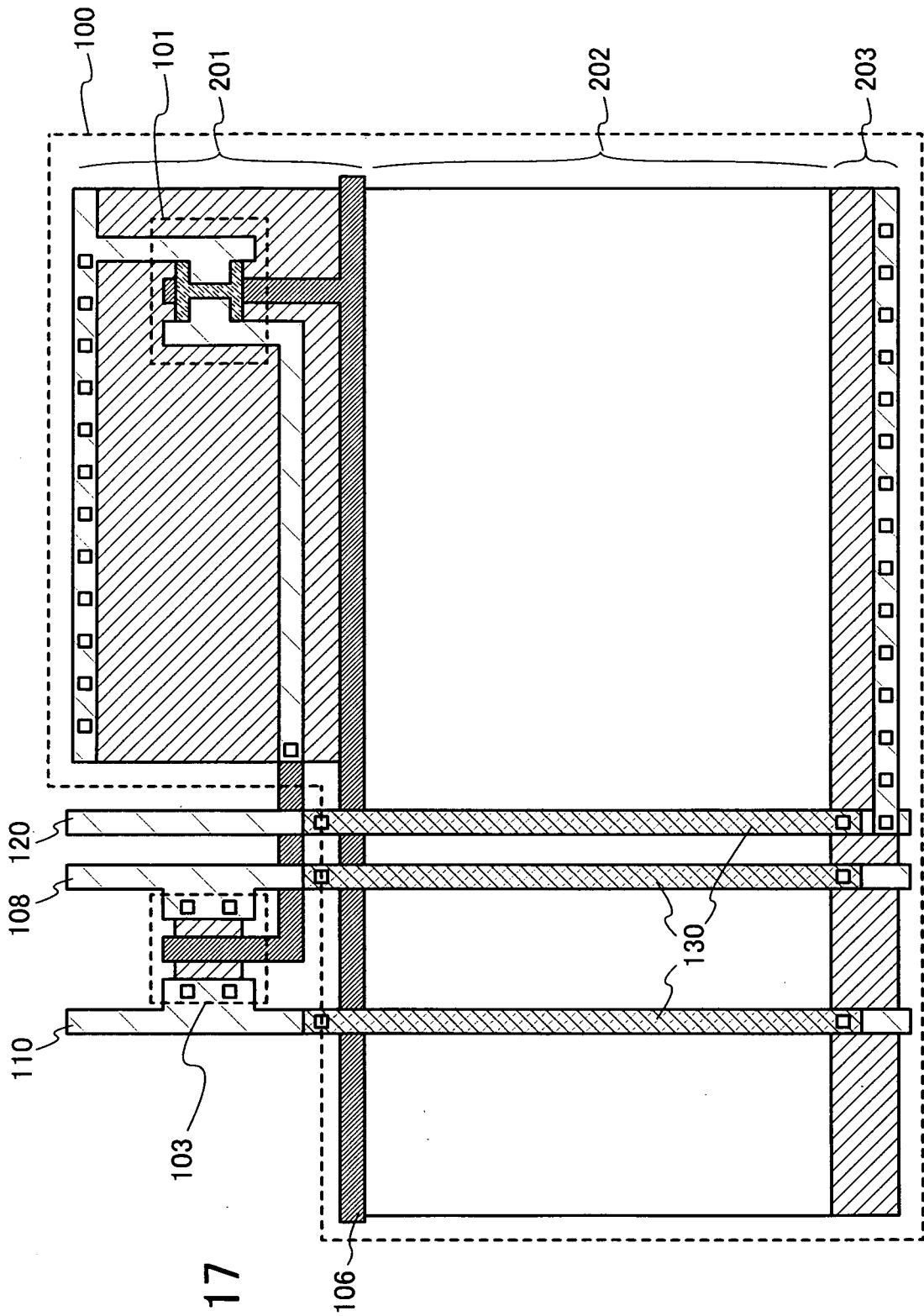


FIG. 17

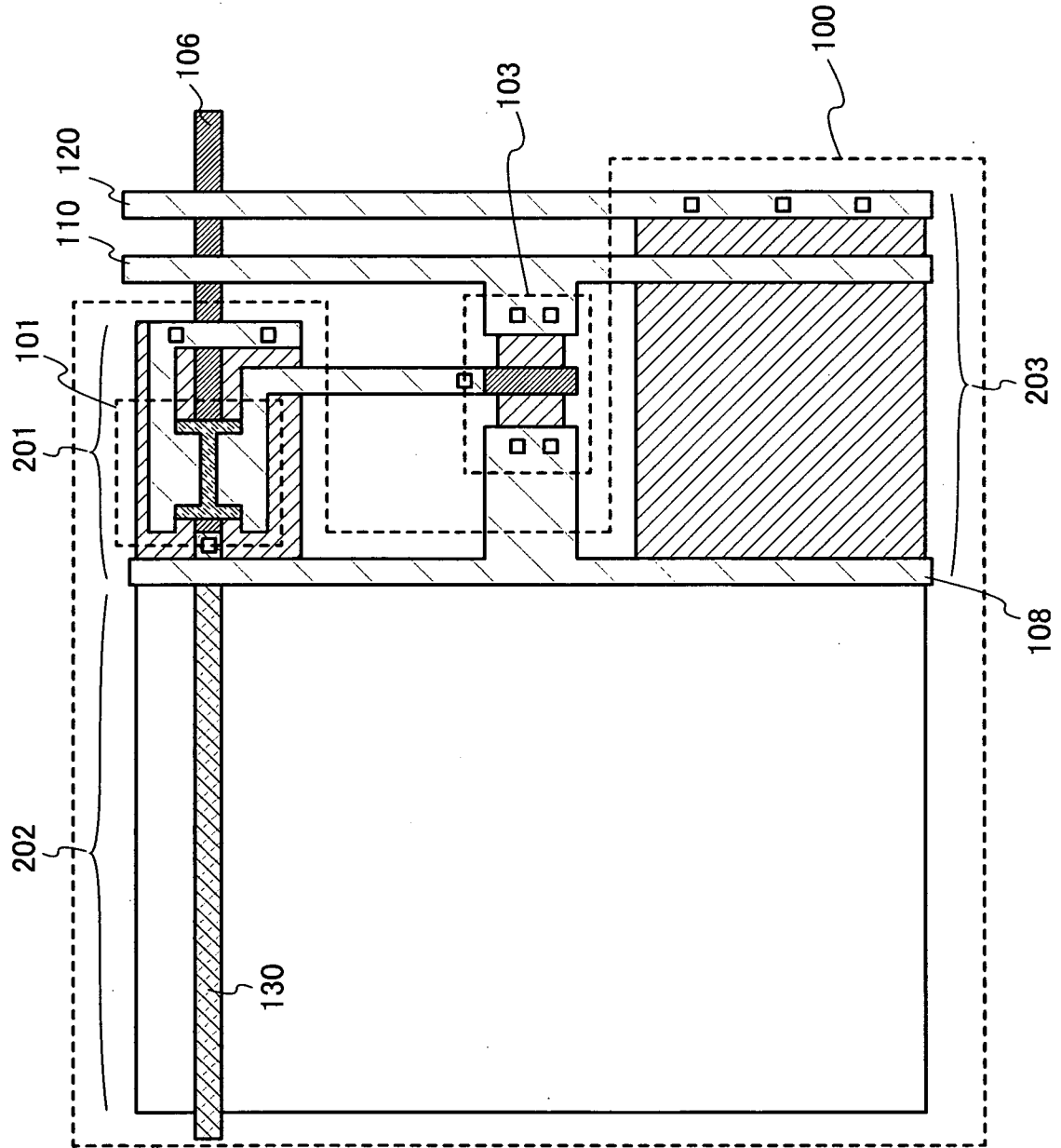


FIG. 18

FIG. 19

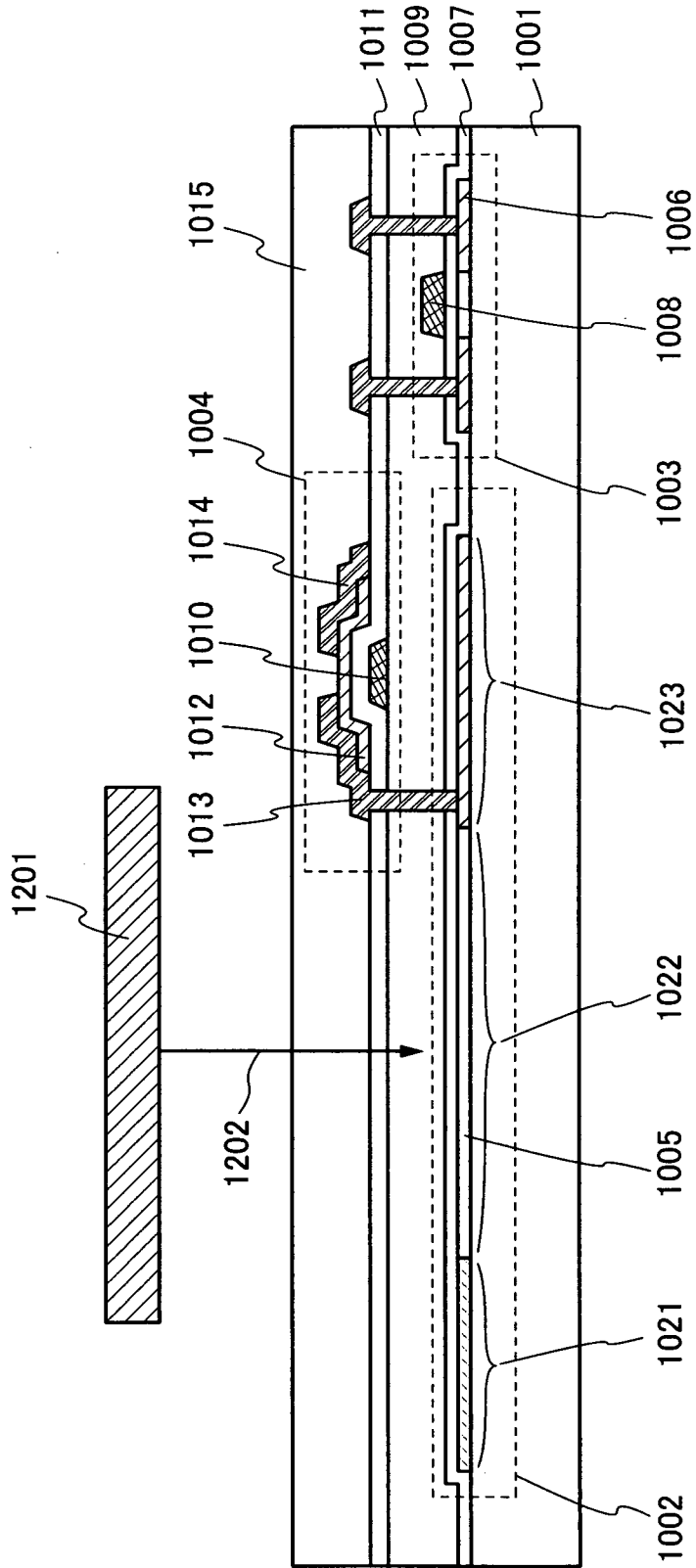
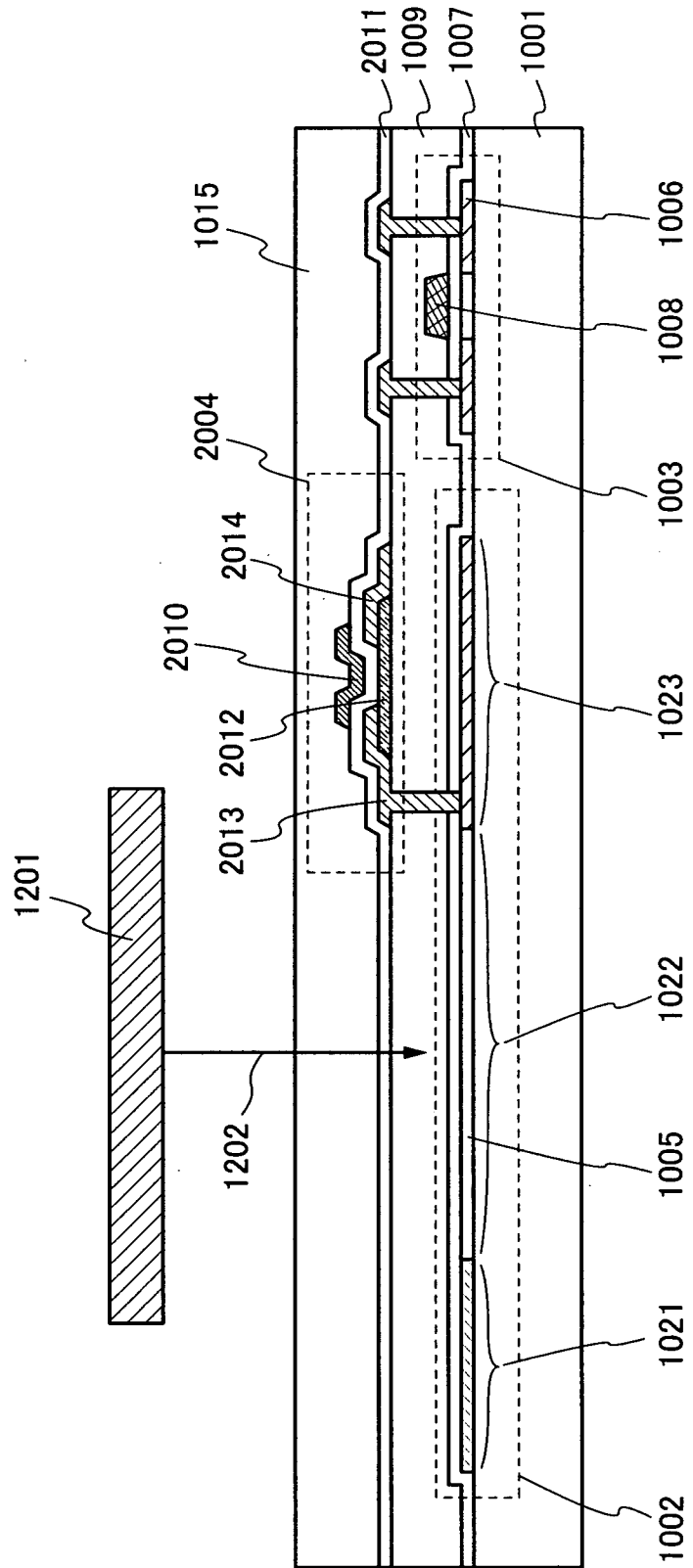


FIG. 20



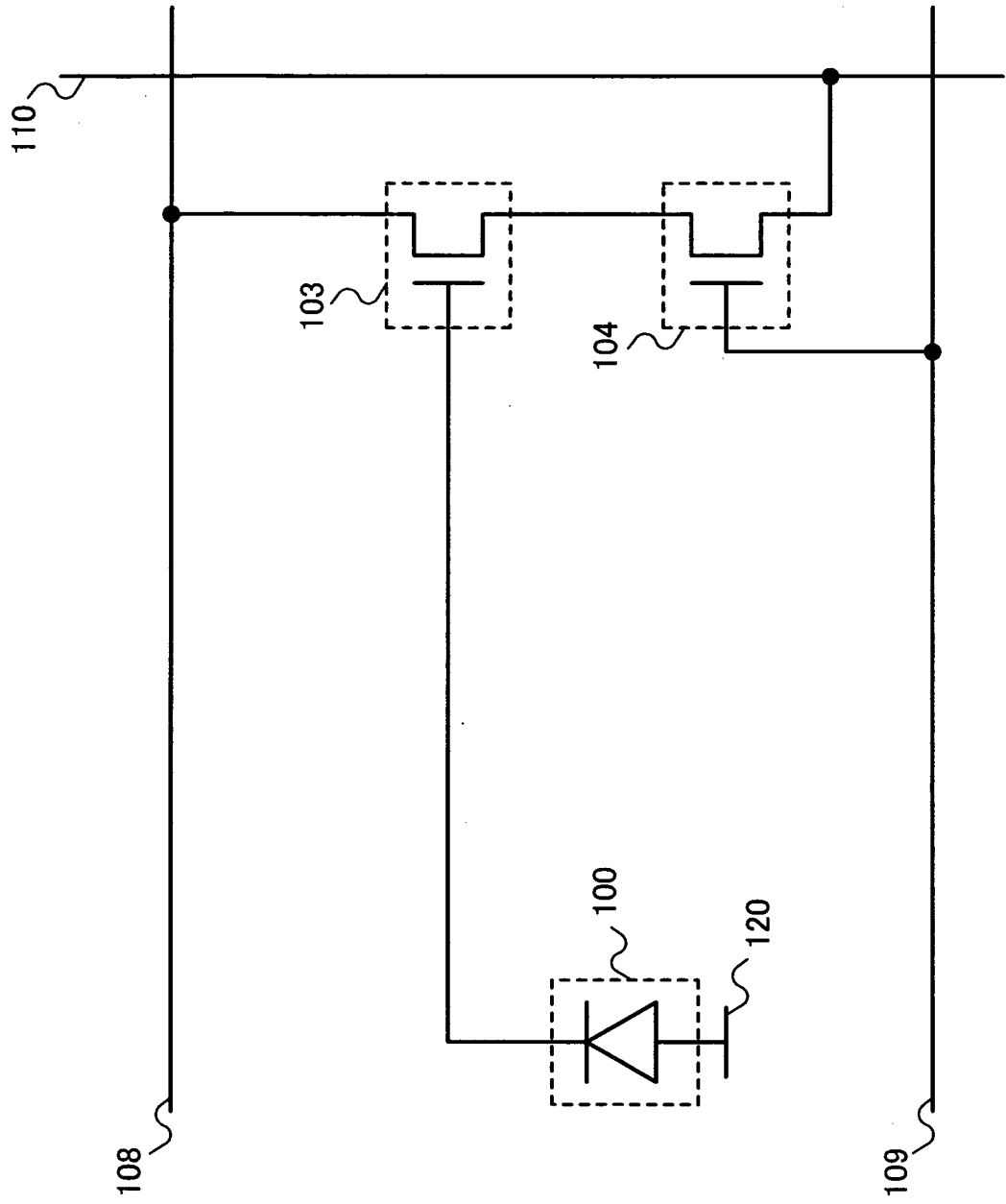


FIG. 21

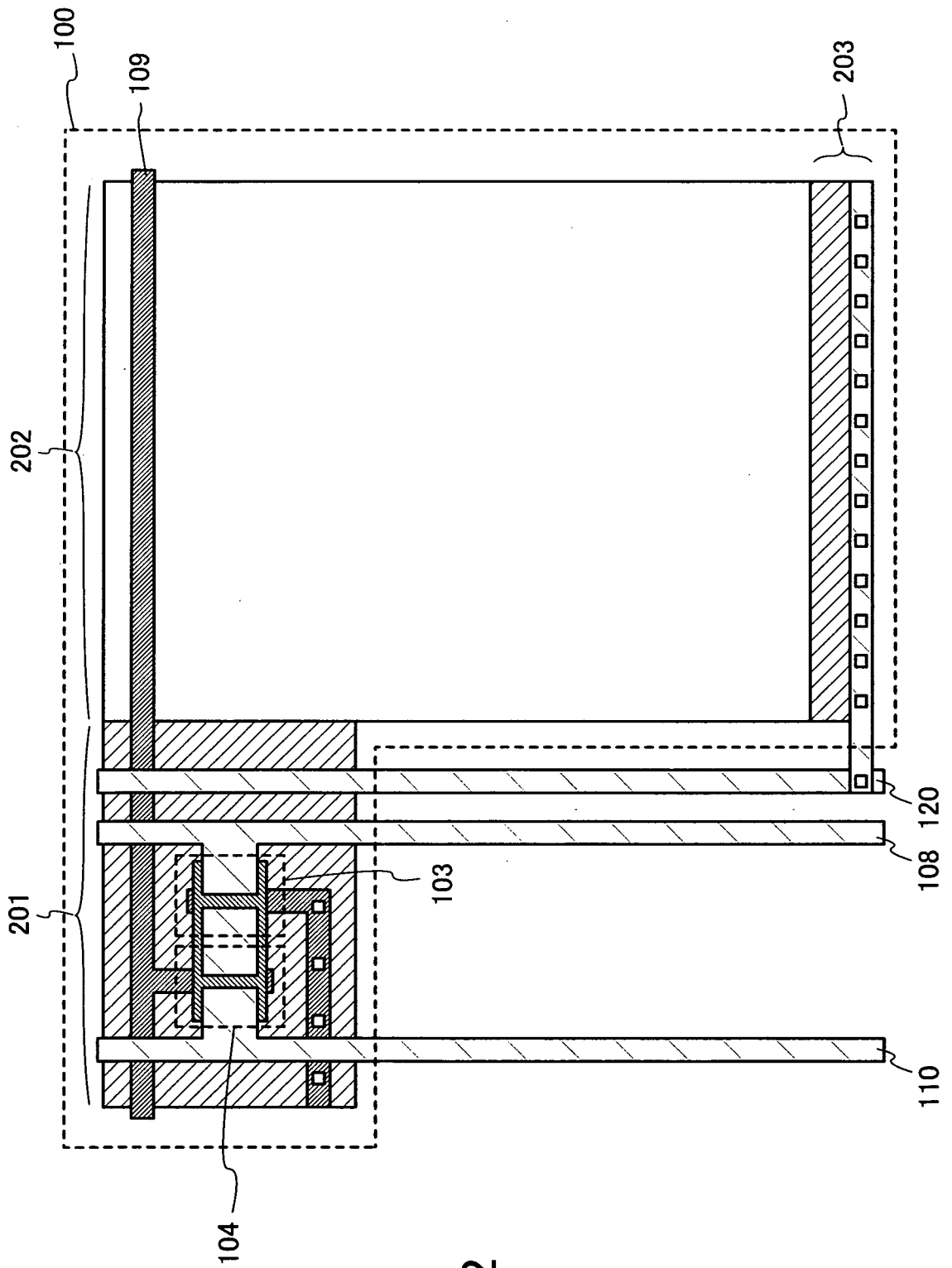
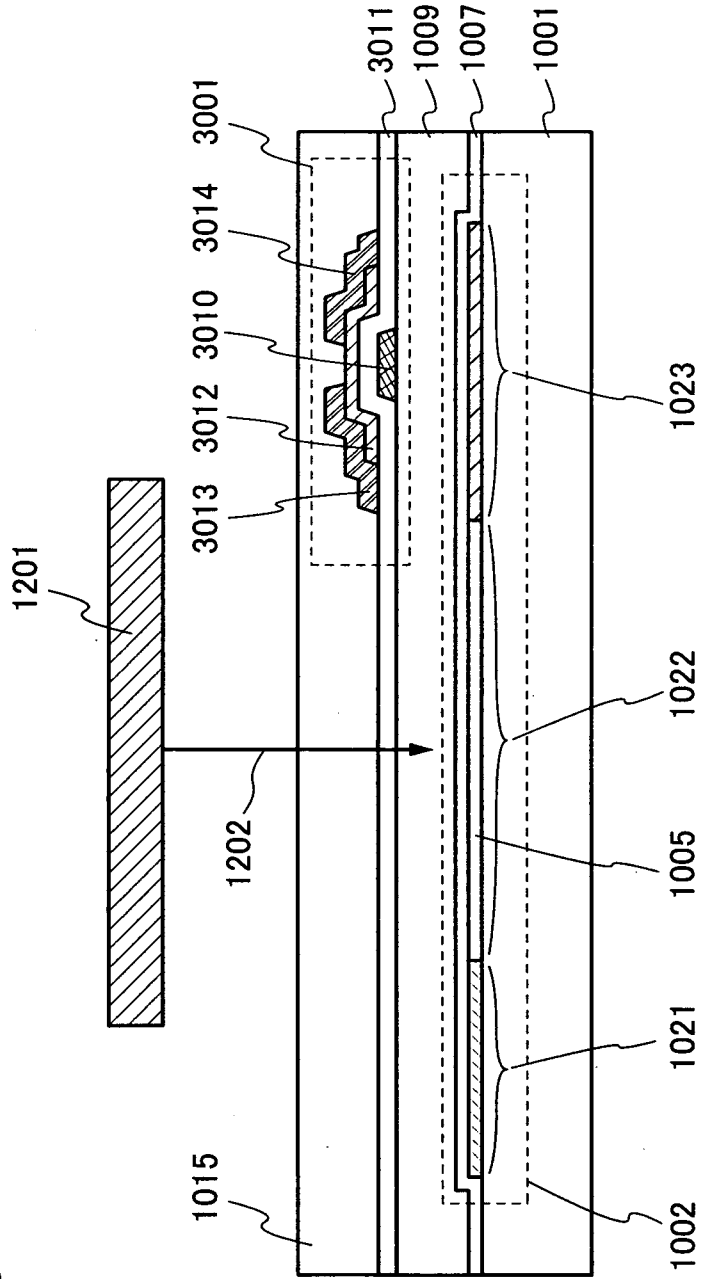
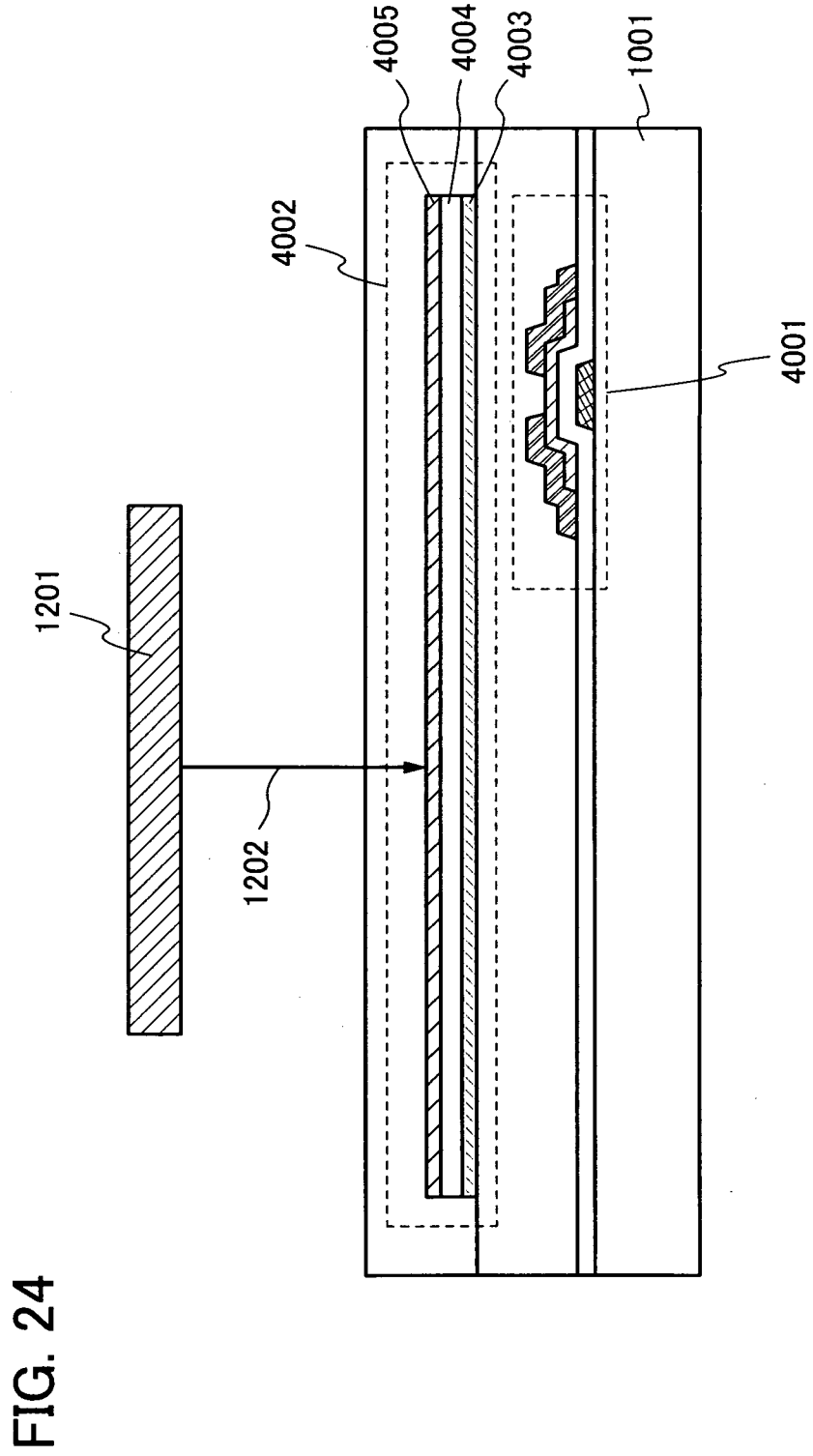


FIG. 22

FIG. 23





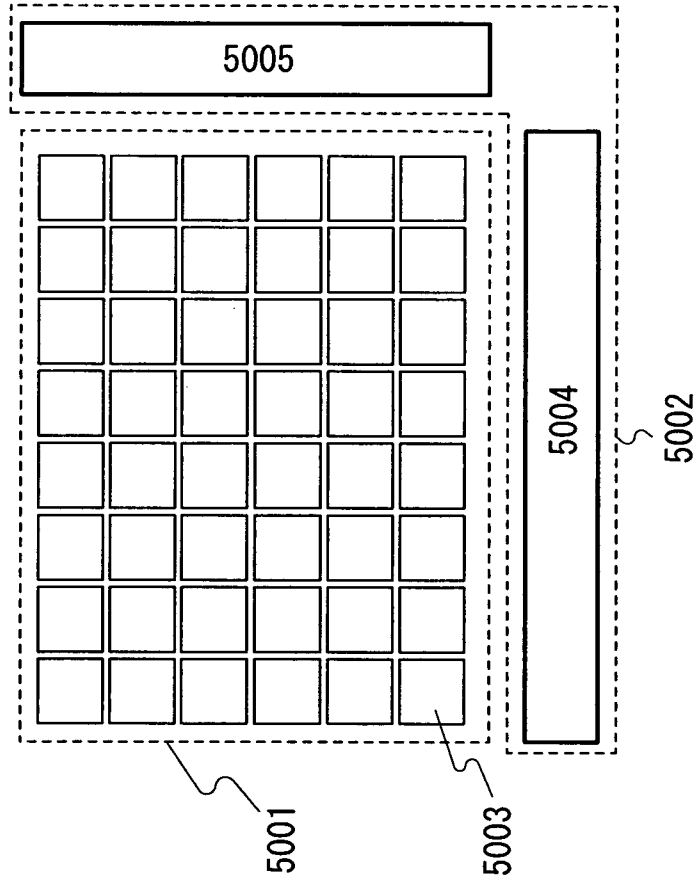


FIG. 25

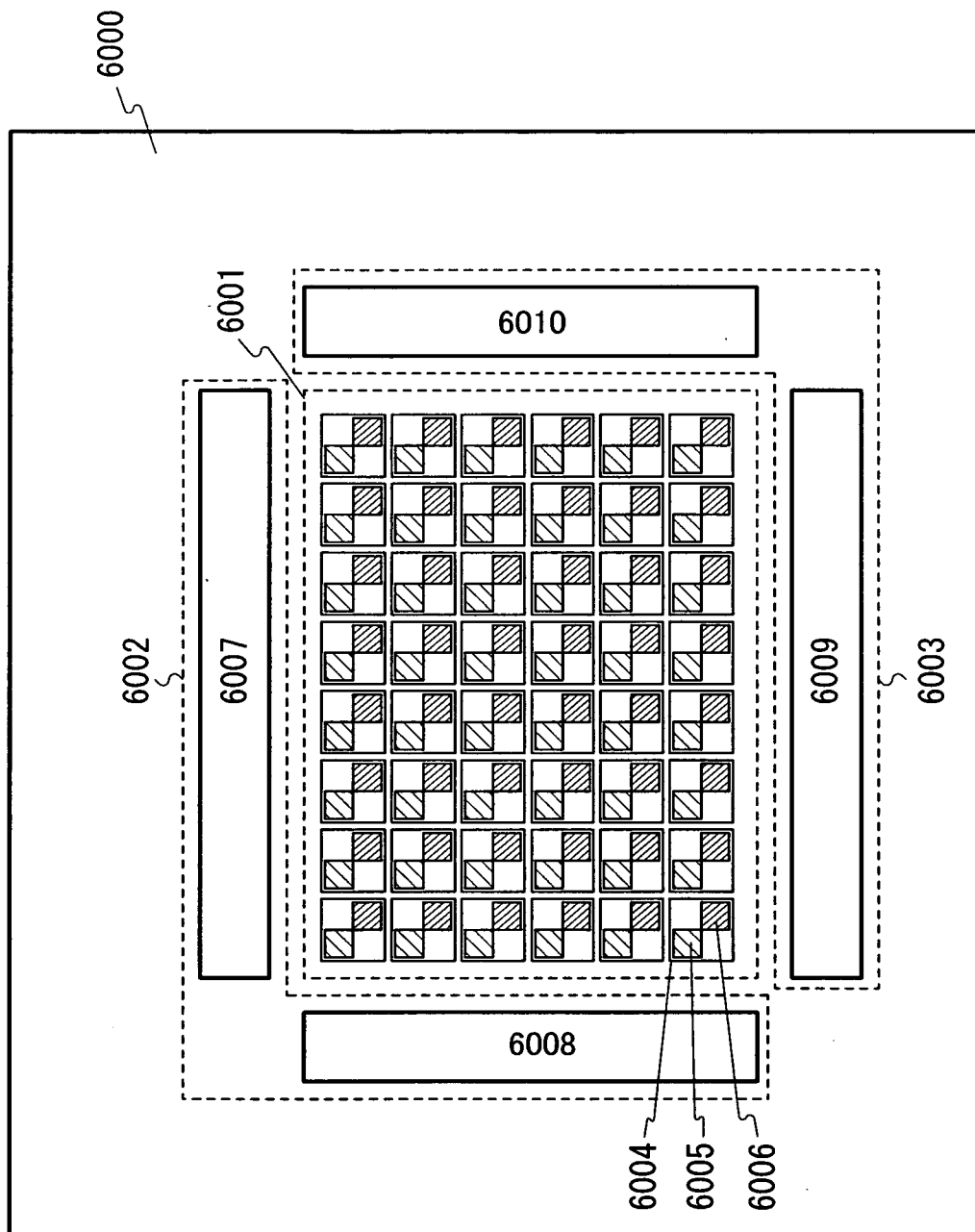


FIG. 26

FIG. 27A

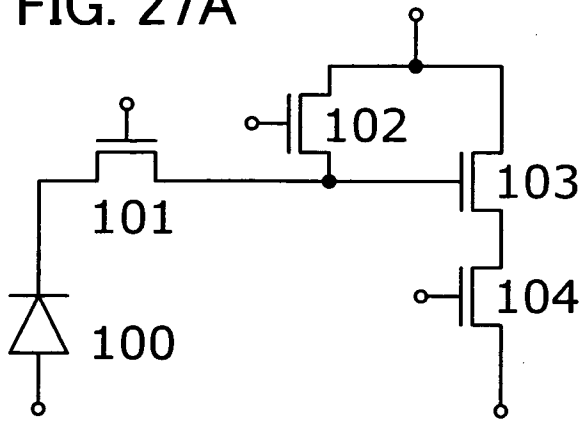


FIG. 27D

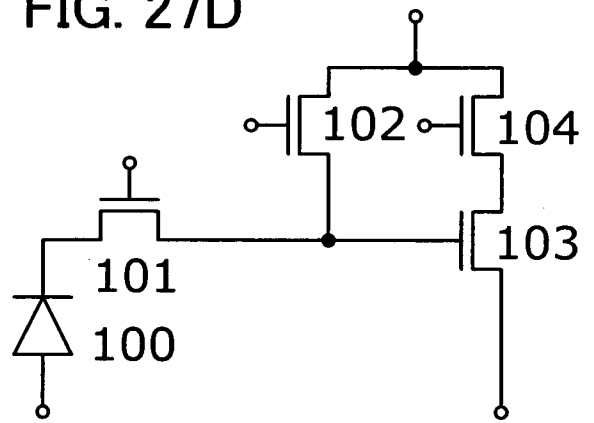


FIG. 27B

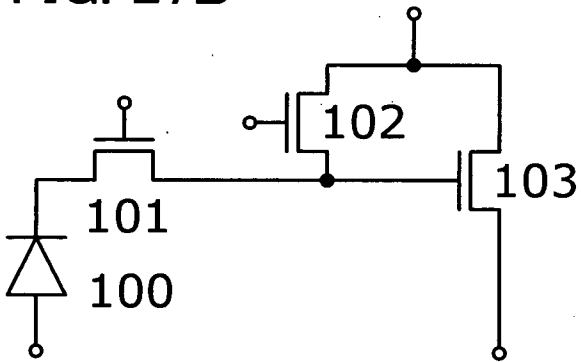


FIG. 27E

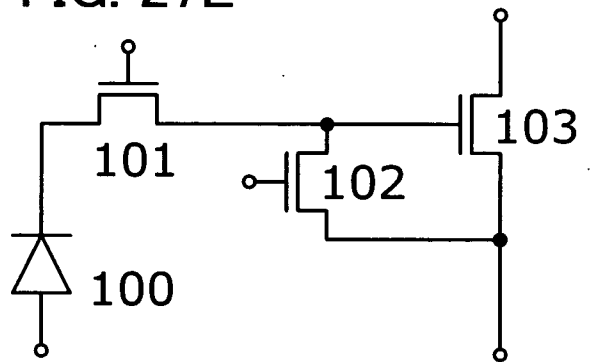


FIG. 27C

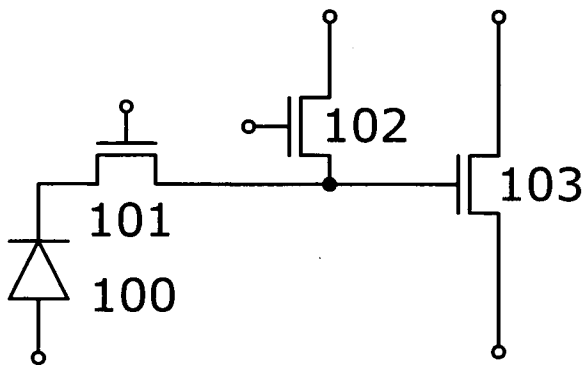


FIG. 27F

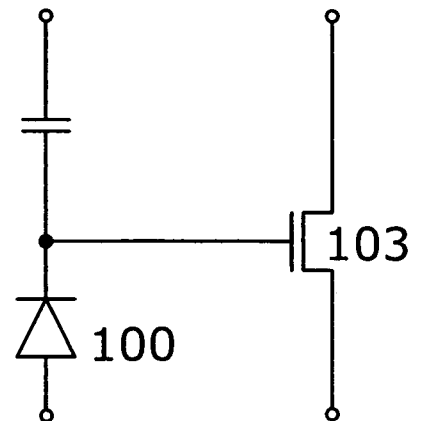


FIG.28

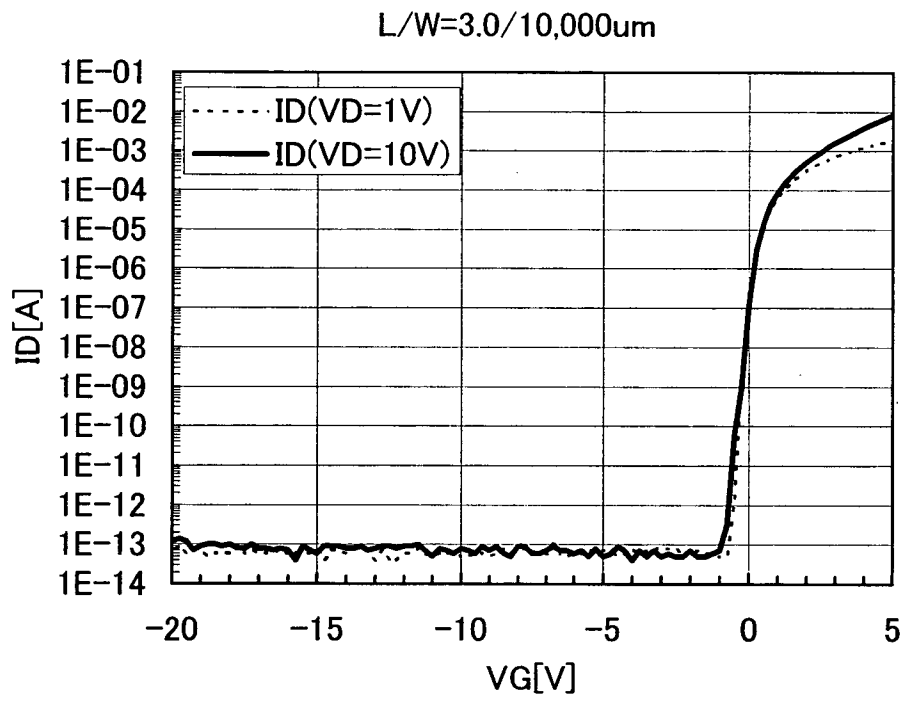
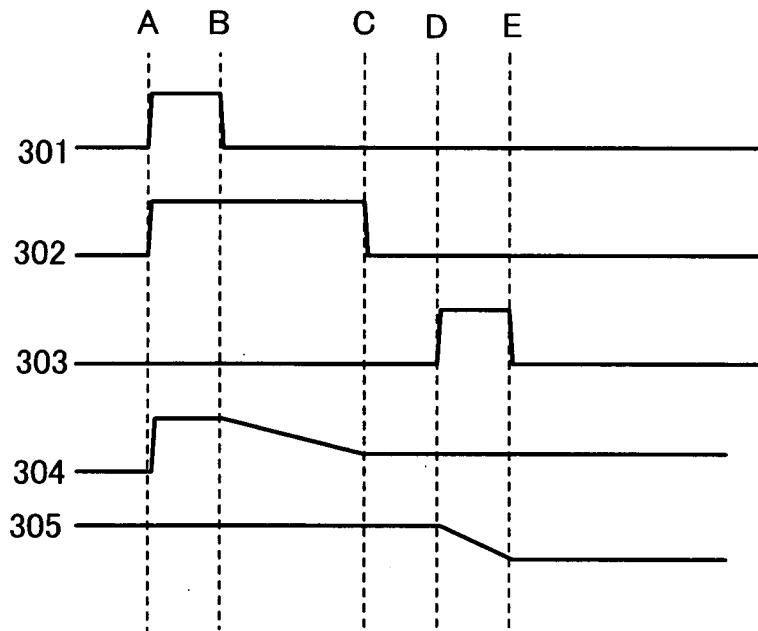


FIG. 29



## EXPLANATION OF REFERENCES

100: photodiode, 101 to 104: transistor, 105 to 110: wiring, 120: wiring, 130: conductive layer, 201: n-region, 202: i-region, 203: p-region, 301 to 305: signal, 1001: substrate, 1002: photodiode, 1003: transistor, 1004: transistor, 1005: semiconductor film, 1006: semiconductor film, 1007: gate insulating film, 1008: gate electrode, 1009: insulating film, 1010: gate electrode, 1011: gate insulating film, 1012: semiconductor film, 1013: electrode, 1014: electrode, 1015: insulating film, 1021: p-region, 1022: i-region, 1023: n-region, 1201: object to be detected, 1202: light, 2004: transistor, 2010: gate electrode, 2011: gate insulating film, 2012: semiconductor film, 2013: electrode, 2014: electrode, 3001: transistor, 3010: gate electrode, 3011: gate insulating film, 3012: semiconductor film, 3013: electrode, 3014: electrode, 4001: transistor, 4002: photodiode, 4003: n-region, 4004: i-region, 4005: p-region, 5001: photosensor portion, 5002: photosensor control circuit, 5003: photosensor, 5004: photosensor reading circuit, 5005: photosensor driver circuit, 6000: display panel, 6001: pixel portion, 6002: display element control circuit, 6003: photosensor control circuit, 6004: pixel, 6005: display element, 6006: photosensor, 6007: display element driver circuit, 6008: display element driver circuit, 6009: photosensor reading circuit, 6010: photosensor driver circuit

## INTERNATIONAL SEARCH REPORT

International application No.

PCT/JP2011/051153

A. CLASSIFICATION OF SUBJECT MATTER		
Int.Cl. H01L27/146(2006.01) i, H04N5/3745(2011.01) i		
According to International Patent Classification (IPC) or to both national classification and IPC		
B. FIELDS SEARCHED		
Minimum documentation searched (classification system followed by classification symbols)		
Int.Cl. H01L27/146, H04N5/3745		
Documentation searched other than minimum documentation to the extent that such documents are included in the fields searched Published examined utility model applications of Japan 1922-1996 Published unexamined utility model applications of Japan 1971-2011 Registered utility model specifications of Japan 1996-2011 Published registered utility model applications of Japan 1994-2011		
Electronic data base consulted during the international search (name of data base and, where practicable, search terms used)		
C. DOCUMENTS CONSIDERED TO BE RELEVANT		
Category*	Citation of document, with indication, where appropriate, of the relevant passages	Relevant to claim No.
X	US 2008/0054319 A1 (Micron Technology, Inc.) 2008.03.06, [0018]-[0037], Fig.2-Fig.6 & JP 2009-535819 A &	1, 2, 4, 6, 8, 10 , 14, 15, 17, 19 , 21, 23
Y	EP 2057684 A & WO 2008/027392 A2 & KR	12, 25
A	10-2008-0094772 A & CN 101366120 A	3, 5, 7, 9, 11, 1 3, 16, 18, 20, 2 2, 24, 26
Y	JP 7-211880 A (NIPPON STEEL CORPORATION) 1995.08.11, [0013], [0015] (No Family)	12, 25
<input checked="" type="checkbox"/> Further documents are listed in the continuation of Box C. <input type="checkbox"/> See patent family annex.		
<p>* Special categories of cited documents:</p> <p>“A” document defining the general state of the art which is not considered to be of particular relevance</p> <p>“E” earlier application or patent but published on or after the international filing date</p> <p>“L” document which may throw doubts on priority claim(s) or which is cited to establish the publication date of another citation or other special reason (as specified)</p> <p>“O” document referring to an oral disclosure, use, exhibition or other means</p> <p>“P” document published prior to the international filing date but later than the priority date claimed</p> <p>“T” later document published after the international filing date or priority date and not in conflict with the application but cited to understand the principle or theory underlying the invention</p> <p>“X” document of particular relevance; the claimed invention cannot be considered novel or cannot be considered to involve an inventive step when the document is taken alone</p> <p>“Y” document of particular relevance; the claimed invention cannot be considered to involve an inventive step when the document is combined with one or more other such documents, such combination being obvious to a person skilled in the art</p> <p>“&amp;” document member of the same patent family</p>		
Date of the actual completion of the international search		Date of mailing of the international search report
24.03.2011		05.04.2011
Name and mailing address of the ISA/JP		Authorized officer
<b>Japan Patent Office</b>		Masahiko YASUDA
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## INTERNATIONAL SEARCH REPORT

International application No.

PCT/JP2011/051153

C (Continuation). DOCUMENTS CONSIDERED TO BE RELEVANT		
Category*	Citation of document, with indication, where appropriate, of the relevant passages	Relevant to claim No.
A	JP 2009-105381 A (SAMSUNG ELECTRONICS., LTD.) 2009.05.14, Whole text, Whole figure & US 2009/0101948 A1 & KR 10-2009-0040158 A & CN 101414615 A	1-26
A	JP 2009-135186 A (SONY CORPORATION) 2009.06.18, Whole text, Whole figure (No Family)	1-26
A	JP 2004-318067 A (TOSHIBA MATSUSHITA DISPLAY TECHNOLOGY KABUSHIKI KAISHA) 2004.11.11, Whole text, Whole figure (No Family)	1-26
A	JP 2009-206508 A (CANON INC.) 2009.09.10, Whole text, Whole figure & EP 2248179 A & WO 2009/096608 A1	1-26
P, A	JP 2010-074138 A (FUJIFILM Corporation) 2010.04.02, Whole text, Whole figure & US 2010/0044711 A & EP 2157615 A1 & CN 101656271 A	1-26