

# NOTICE OF ENTITLEMENT

I (We) John H. LeFevre  
authorised by Wang Laboratories, Inc.  
of One Industrial Avenue, Lowell, MA 01851  
the applicant in respect of an application for a patent for an invention entitled  
GRAPHICS SURFACE ASSEMBLY WITH CALIBRATING MEMORY DEVICE  
filed under Australian Application No. 12522/92, state the following:

**PART 1 - Must be completed for all applications.**

The person(s) nominated for the grant of the patent

is (are) the actual inventor

or

has, for the following reason, obtained entitlement from the actual inventor(s)

The nominated person is the assignee of the invention from the actual inventor.

**PART 2 - Must be completed if the application is a Convention application.**

The person(s) nominated for the grant of the patent is (are):

the applicant(s) of the basic application(s) listed on the patent request form

or

entitled to rely on the basic application(s) listed on the patent request form by reason of the following:

The nominated person is the assignee of the invention from the actual inventor.

The basic application(s) listed on the request form is (are) the first application(s) made in a Convention country in respect of the invention.

**PART 3 - Must be completed if the application was made under the PCT and claims priority.**

The person(s) nominated for the grant of the patent is (are):

the applicant(s) of the application(s) listed in the declaration under Article 8 of the PCT

or

entitled to rely on the application(s) listed in the declaration under Article 8 of the PCT by reason of the following:

The nominated person is the assignee of the basic application from the inventor(s)

The basic application(s) listed in the declaration made under Article 8 of the PCT is (are) the first application(s) made in a Convention country in respect of the invention.

Dated this 17th day of August, 1993

Signed John H. LeFevre Status Vice President

Signatory's Name John H. LeFevre



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controller (35);

attaching the flex cable (28,30,34;128) to the electro-  
graphic surface (22);

attaching the memory device (32) to the flex cable  
(28,30,34;128);

generating calibration data for the electrographic sur-  
face (22); and

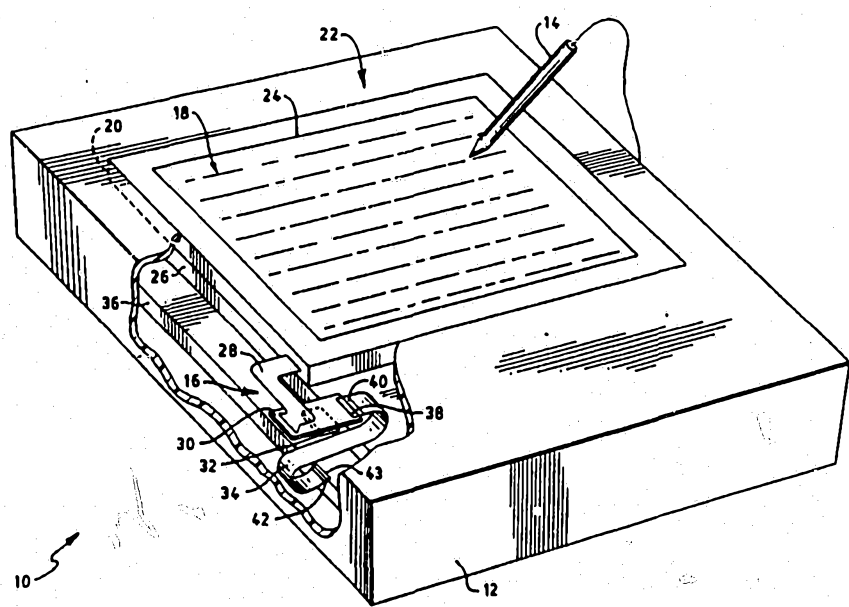
storing the calibration data in the memory device (32).

INTERNATIONAL

N TREATY (PCT)

<p><b>(51) International Patent Classification <sup>5</sup> :</b>  <b>G06K 11/06, G06F 3/033</b>  <b>G08C 21/00</b></p>	<p><b>A1</b></p>	<p><b>(11) International Publication Number:</b> <b>WO 92/16911</b>  <b>(43) International Publication Date:</b> 1 October 1992 (01.10.92)</p>
<p><b>(21) International Application Number:</b> PCT/US91/07898  <b>(22) International Filing Date:</b> 24 October 1991 (24.10.91)  <b>(30) Priority data:</b>          675,250 25 March 1991 (25.03.91) US  <b>(71) Applicant:</b> WANG LABORATORIES, INC. [US/US];          One Industrial Avenue, M/S 014-B7D, Lowell, MA          01851 (US).  <b>(72) Inventor:</b> DRUMM, Donald, E. ; 4 Frost Street, Billerica,          MA 01821 (US).  <b>(74) Agents:</b> SHANAHAN, Michael, H. et al.; One Industrial          Avenue, MS 014-B7D, Lowell, MA 01851 (US).</p>	<p><b>(81) Designated States:</b> AT (European patent), AU, BE (European patent), CA, CH (European patent), DE (European patent), DK (European patent), ES (European patent), FR (European patent), GB (European patent), GR (European patent), IT (European patent), JP, LU (European patent), NL (European patent), SE (European patent).</p> <p><b>Published</b>  <i>With international search report.</i></p> <p style="text-align: center; font-size: 2em; font-weight: bold;">659628</p>	

**(54) Title: GRAPHICS SURFACE ASSEMBLY WITH CALIBRATING MEMORY DEVICE**



**(57) Abstract**

An electrographic system comprises a graphics surface (22) assembly having a graphics surface integrally coupled to a memory device which contains calibration data designated for the graphics surface. During manufacturing, the memory device (32) is mounted onto an interface board (30) which is attached to the graphics surface through a flex cable (28). A calibration procedure is performed on the graphics surface whereby calibration data is generated and stored in the memory device. The memory device, loaded with the calibration data, is thereafter linked to its associated graphics surface.

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GRAPHICS SURFACE ASSEMBLY WITH  
CALIBRATING MEMORY DEVICE

BACKGROUND OF THE INVENTION

Conventional computers offer a full range of features including a hard disk drive, a floppy disk drive, a microprocessor, a liquid crystal display (LCD), and a keyboard. Typically, the user interface for conventional computers comprises the keyboard, serving as the input device, and the LCD serving as the output device. In recent years, there has been a considerable amount of progress in broadening the functional capability of the user interface of computers. This progress has led to the development of a computer system having an enhanced user interface comprising an LCD, an input device such as a stylus and an electromagnetic graphics surface positioned over the LCD. Computers having this type of enhanced user interface are commonly referred to as electrographic systems or graphics tablets. Operation of a graphics tablet involves positioning the stylus relative to the graphics surface. The graphics surface senses the position of the stylus and generates analog coordinate signals which are digitized by electronics in the graphics tablet and stored in a memory device. The stored digital data may be processed and displayed on the LCD or even sent to a printer.

Currently, graphics tablets operate based on capacitive coupling of the stylus with a position responsive graphics surface to generate output signals. Capacitive coupling may be achieved with a graphics

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surface having either a grid layer formed of orthogonal linear arrays of spaced conductors or a layer of electrically resistive material.

Significant advantages have been observed in developing a graphics surface having a layer of resistive material, known as a resistive surface device. One advantage is the inherent simplicity in producing a graphics surface which comprises only a layer of resistive material upon a supportive insulative substrate such as glass or plastic. Also, graphics surfaces have expanded industrial applications where the resistive layer and the substrate are transparent.

A variety of technical problems have hindered the development of graphics tablets employing resistive surface devices. One such problem involves the non-uniform nature of the coordinate readouts achieved with the resistive material. Precise linearity is required between the stylus position and the coordinate signals to achieve accurate readouts. Since resistive materials cannot be applied to a substrate without some local resistance variations, the non-linear aspects inherent in this approach have impeded the use of resistive surface devices in applications which demand a very high degree of accuracy. Applications that require highly accurate devices include computer graphics, computer-aided design and computer-aided manufacturing systems.

Several technical advances in utilizing resistive surface devices despite surface non-linearities have been achieved. As an example, substantially improved accuracy for resistive surface devices has been realized through a correction procedure described in U.S. Patent No. 4,650,926 to Nakamura et al. After manufacturing

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graphics tablets having a resistive graphics surface, each resistive surface undergoes a calibration procedure such that calibration data for the graphics surface is generated. The calibration data is based on the unique non-linearities associated with a particular resistive surface and is stored in a memory device. The memory device is coupled to a controller board in an electrographics system employing that graphics surface. During the operation of the graphics tablet, the controller board provides four drive signals to the graphics surface. Position coordinate data for the stylus is determined by the controller based on the actual position of the stylus. The controller then employs a correction procedure whereby a given coordinate pair is corrected using calibration data taken from the memory device.

#### SUMMARY OF THE INVENTION

In existing electrographic systems, the calibration data is loaded into a memory device during the aforementioned calibration procedure. The memory device must then be tracked along with its associated graphics surface during the manufacturing process. As an example, the memory device may be affixed with an identification number which corresponds to a serial number on the associated graphics surface. Eventually, the memory device and its associated graphics surface are mounted together in a graphics tablet.

The calibration data compiled for a graphics surface and stored in a memory device has not previously been used in a mass produced graphics tablet. However, difficulties are anticipated in mass production of graphics tablets with respect to tracking each memory

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device and its associated graphics surface. Such tracking is essential in assuring that the proper memory device is mounted in a graphics tablet housing with the corresponding graphics surface. In the area of field service, additional difficulties are anticipated to arise when a graphics surface of a prior art graphics tablet requires replacement. Because each graphics surface is unique, the associated memory device mounted on the controller board also must be replaced. Field service personnel can readily remove and replace the graphics surface. The memory device, however, is mounted with the controller electronics on the main electronics board within the graphics tablet housing and is difficult to replace without replacement of the entire controller board.

The present invention solves the aforementioned problems by providing a graphics surface assembly having a memory device integrally coupled to its associated graphics surface. The two are coupled throughout the manufacturing process subsequent to calibration of the graphics surface and storage of calibration data in the memory device. It is not feasible, however, to couple the graphics surface to the controller board throughout manufacturing.

The graphics surface is preferably formed of a conductive layer and an insulative support substrate. Typically, communications with the graphics surface are through a flex cable which is attached to the graphics surface prior to calibration. The flex cable allows for the difficult electrical connection to the conductive layer of the graphics surface. The flex cable also provides the electrical connection to the graphics

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surface during calibration and thereafter remains connected to the graphics surface.

In accordance with the present invention, the memory device which stores the calibration data for subsequent use by the controller board is integrally coupled to the flex cable. Since the flex cable is electrically connected to the controller board in the final assembly, so is the memory device, preferably through a single connector carrying both the graphics surface control signals and the memory device address and data signals. Thus, in subsequent field-service of the graphics surface assembly, the memory device and the graphics surface may be replaced together.

In a preferred embodiment, the memory device is mounted on an interface board which is remotely located with respect to the controller electronics in the final assembly. The interface board has signal lines for communications with the graphics surface as well as additional signal lines connected to the memory device. Prior to calibration of the graphics surface, the flex cable is attached to the interface board forming a unitary structure. A calibration test procedure is then performed on the resistive layer, and calibration data for that resistive layer is stored in the memory device. The interface board has a single connector which carries both the graphics surface control signals and the memory device address and data signals and is coupled to the controller board in the final assembly via a ribbon cable.

In a alternative embodiment of the present invention, a graphics surface assembly comprises a graphics surface attached to a flex cable having additional signal lines printed thereon and having the

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memory device mounted directly to the flex cable. The memory device, mounted to the flex cable, is loaded with the calibration data during calibration and is thereafter linked to that graphics surface to which it must be associated. Like the preferred embodiment, the resulting graphics surface assembly eliminates any further requirement for tracking the memory device loaded with the calibration data and its associated graphics surface during the manufacturing process. With the alternative embodiment, the graphics surface and the memory device may also be replaced together.

#### BRIEF DESCRIPTION OF THE DRAWINGS

In the enclosed drawings like reference characters refer to the same parts throughout the different views. The drawings are not necessarily to scale, emphasis instead being placed upon illustrating the principles of the present invention.

FIG. 1 is a perspective view of an electrographic tablet with a cut-away section illustrating a graphic surface assembly of the present invention.

FIG. 2 is an enlarged perspective view of the graphic surface assembly of FIG. 1.

FIG. 3 is an enlarged plan view of the interface board of FIG. 2.

FIG. 4 is an enlarged plan view of an alternative embodiment of the graphics surface assembly of FIG. 2.

#### DETAILED DESCRIPTION OF THE INVENTION

An electrographic apparatus for application of the present invention is shown in FIG. 1 and described herein. The apparatus comprises a computer 10, or

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graphics tablet, having a housing 12 which encloses a full microprocessor-based computer system. The user interface is by means of a liquid crystal display 26 and a member 14 which may be a stylus. The position of the member relative to the display is sensed by a transparent graphics surface 22 placed over the display 26. The graphics tablet 10 responds to the position of the member 14 by generating coordinate data.

In accordance with the present invention, a graphics surface assembly comprises a graphics surface 22 having a preferred resistive layer 18 deposited on the back surface of a transparent support substrate 20. The resistive layer 18 is formed of indium tin oxide (ITO), and the support substrate 20 is formed of glass or plastic. The graphics surface 22 resides in the housing 12 adjacent to the liquid crystal display (LCD) 26 which is viewed through the graphics surface.

The graphics surface assembly also comprises a flex cable 28 attached to the graphics surface. The flex cable allows for the difficult electrical connection to the resistive layer of the graphics surface. The flex cable also provides the electrical connection to the graphics surface during calibration as explained later. The flex cable has four signal lines for providing a.c. drive signals to the graphics surface. Electrodes 24 are coupled to the signal lines on the graphics surface for applying the drive signals to the resistive layer 18. Preferably, the controller electronics 35 located on a main electronics board 36 generates the drive signals for the resistive layer. These drive signals are used in conjunction with the resistive layer and the member 14 to generate analog coordinate signals corresponding to the position of the member. More specifically, the

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controller electronics uses the voltage sensed by the member in conjunction with a source switching process to generate a pair of analog coordinate signals. As an example, a signal corresponding to coordinates (x,y) is translated to analog position data by the following process. The a.c. source signal is applied in one coordinate direction to one edge of the resistive layer while a ground reference is applied to the opposite edge, and the voltage pickup at a stylus is measured. The a.c. signal and ground reference are then reversed for that coordinate direction and the voltage sensed by the member is again measured. The two voltages are manipulated by the controller electronics to determine one coordinate. The process is then repeated for the orthogonal coordinate direction such that the (x,y) coordinates are determined.

The analog coordinate signals are converted to digital position signals by the controller electronics. The controller electronics corrects for errors associated with position signals due to variations in the resistive layer by employing calibration data generated for that resistive layer and stored in a memory device 32. In a preferred embodiment of the present invention, the graphics surface assembly further comprises an interface board 30 having the memory device 32 mounted thereon. The memory device is preferably a Programmable Read Only Memory (PROM) device and is electrically linked to signal lines on the interface board. These signal lines are further linked to signal lines of a ribbon cable 34 via the mated connector pair 38 and 40. The ribbon cable comprises a plurality of signal lines which are linked to the controller electronics by a second mated connector

pair 42 and 43. As a result, the PROM is electrically linked to the controller electronics 35.

In the preferred embodiment, the interface board is attached to the flex cable and provides the flex cable with the a.c. drive signals which originate in the controller electronics. As explained above, the ribbon cable 34 provides an electrical link between the interface board and the controller electronics (see FIG. 3). As such, the a.c. drive signals from the controller electronics 35 are sent along the ribbon cable and the interface board to the flex cable and the graphics surface.

The calibration data for each graphics surface is typically generated during the manufacturing process of a graphics tablet. The aforementioned correction procedure is performed such that the graphics surface is tested and calibration data generated for that graphics surface is stored in a PROM. In existing systems, prior to mounting the PROM and its associated graphics surface in a graphics tablet housing, both components are separately tracked. As an example of such tracking, the memory device might be affixed with an identification number corresponding to a serial number on the associated graphics surface. Since the resistive layer of each graphics surface is unique, each PROM containing the calibration data must be tracked along with its associated graphics surface. However, problems have been recognized with separate tracking of a PROM and its associated graphics surface. For example, a PROM may be rejected during quality assurance testing on an assembled graphics tablet which is performed after the calibration procedure. A graphics surface associated with the

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rejected PROM must be recalibrated so that the calibration data may be loaded into a replacement PROM.

As shown in FIG. 2, the present invention eliminates the aforementioned tracking problems during the manufacturing process by providing a graphics surface assembly 16 having the PROM 32 integrally coupled to its associated graphics surface 22. In the final stages of the manufacturing process for a graphics surface, the PROM 32 is mounted onto the interface board 30, the flex cable 28 is attached to the graphics surface 22 and the flex cable is attached to the interface board. A calibration test procedure is subsequently performed on the resistive layer 18 of the graphics surface and calibration data is generated. The PROM is loaded with this data during the calibration procedure. Unlike existing systems, the PROM loaded with the calibration data is thereafter linked to its associated graphics surface 22. Thus, no further separate tracking of the PROM and the graphics surface is required for the remainder of the manufacturing process of the graphics tablet.

Other problems have been recognized with prior art graphics tablets in the area of field service, specifically when a graphics surface must be replaced. Such replacement is required if a graphics surface is defective or damaged. Since the resistive layer of each graphics surface is unique, replacement of a graphics surface in the field requires replacement of the associated PROM. Field service personnel are able to replace the graphics surface, since it is readily accessible. The PROM, however, is not generally accessible to field service personnel as it is mounted with the controller electronics on the main electronics

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board in existing systems. Even if the PROM were accessible, it would not be desirable to work on the main electronics board to replace an individual component as field maintenance is generally limited to board-level replacement. Thus, the main electronics board would have to be replaced, or the entire graphics tablet would have to be returned to the factory to accomplish the repairs.

An important feature of the present invention is that the graphics surface assembly (shown in FIG. 2) is a unitary structure having the PROM with calibration data therein integrally coupled to its associated graphics surface. Accordingly, when a graphics surface of the present invention requires field replacement, field service personnel can replace both the graphics surface and the PROM by simply replacing the graphics surface assembly.

Unlike existing electrographic systems having the PROM located with the controller electronics on the main electronics board, in the present invention the PROM is mounted on the interface board which is remotely located with respect to the controller electronics. Even though the PROM is remotely located, the graphics tablet comprising the present invention is electrically equivalent to existing systems. As shown in FIG. 3, the PROM 32 is mounted on the interface board 30. Support electronics represented at 44 and 46 facilitates communications between the PROM and the controller electronics. The support electronics may comprise a multiplexer 44 and a logic device 46 in a preferred embodiment. The multiplexer multiplexes addresses sent to the PROM and calibration data from the PROM sent to the controller electronics. The logic device provides enable signals to the PROM. In order to keep the parts

count down for the system, the PROM is loaded with a handwriting recognition program in addition to the calibration data for a graphics surface. The interface board has signal lines connecting the PROM and support electronics to the ribbon cable 34 by the mated connector pair 38 and 40. As explained previously, the ribbon cable is linked to the controller electronics by the second mated connector pair 42 and 43 (see FIG. 1). Thus, the PROM and support electronics are electrically linked to the controller electronics via the ribbon cable 34. As such, the graphics tablet having its PROM remotely located on an interface board is electrically equivalent to existing systems but comprises the above-described features unique to the present invention.

In an alternative embodiment of the present invention shown in FIG. 4, a graphics surface assembly 116 comprises a graphics surface 22 attached to a flex cable 128 having additional signal lines printed thereon and having a PROM 32 mounted directly onto the flex cable. The PROM 32, mounted to the flex cable, is loaded with the calibration data generated for the graphics surface 22 during calibration and is thereafter linked to that graphics surface. During manufacturing of electrographic systems, the alternative embodiment of the graphics surface assembly eliminates any further requirement for tracking the memory device loaded with calibration data and its associated graphics surface. The graphics surface assembly of this embodiment is also a unitary assembly, so field service personnel may replace the entire assembly.

In accordance with this embodiment, a graphics surface 18 comprises a resistive layer 18 on an insulative support substrate 20 such as glass or plastic.

The resistive layer is preferably formed of indium tin oxide and has a non-linear surface resistance. Alternatively, the graphics surface may comprise a material having a non-linear surface capacitance on an insulative support substrate. The flex cable 128, which is connected to the main electronics board via connector 142, employs four signal lines for providing a.c. drive signals to the resistive layer of the graphics surface. An additional 20 signal lines on the flex cable provide a connection between the PROM 32 and the controller electronics located on the main electronics board. As in the preferred embodiment, the PROM contains the calibration data for the graphics surface and a handwriting recognition program for the graphics tablet. The PROM functions electrically as if it were still mounted on the controller board, though it is located on the flex cable in this embodiment.



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CLAIMS

1           1. An electrographic surface assembly comprising:  
2           an electrographic surface (22) providing possibly non-  
3           linear position indications of a member (14);  
4           a first cable (28,30,34;128) connected to the electro-  
5           graphic surface (22) and having signal lines for communica-  
6           tions between the electrographic surface (22) and a controller  
7           (35) located remotely on a main electronics board (36); and  
8           a memory device (32) having calibration data which the  
9           controller (35) may access to compensate for the non-linearity  
10          of the electrographic surface (22) in providing position  
11          indications of the member (14) CHARACTERIZED BY:  
12          said memory device being integrally coupled to the first  
13          cable.

1           2. An electrographic surface assembly as claimed in Claim  
2           1 further CHARACTERIZED BY an interface board (30) integrally  
3           connected to the first cable (28,30,34), the interface board  
4           (30) having signal lines for communications between the elect-  
5           rographic surface (22) and the controller (35), the interface  
6           board (30) having the memory device (32) mounted thereon, and  
7           the interface board (30) having additional signal lines for  
8           communications between the memory device (32) and the control-  
9           ler (35).

1           3. An electrographic surface assembly as claimed in Claim  
2           2 further CHARACTERIZED BY a second cable (34) connected to  
3           the interface board (30), the second cable (34) having signal  
4           lines for communications with the controller (35).



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1 4. An electrographic surface assembly as claimed in Claim  
2 1 CHARACTERIZED IN THAT the memory device (32) is directly  
3 mounted on the first cable (128), the first cable (128) having  
4 additional signal lines for communications between the memory  
5 device (32) and the controller (35).

1 5. An electrographic surface assembly as claimed in Claim  
2 1 CHARACTERIZED IN THAT the memory device (32) is a read only  
3 memory device (32).

1 6. An electrographic surface assembly as claimed in Claim  
5 CHARACTERIZED IN THAT the memory device (32) is a programma-  
ble read only memory device (32).

1 7. An electrographic surface assembly as claimed in Claim  
2 1 CHARACTERIZED IN THAT the electrographic surface (22) in-  
3 cludes a resistive layer on an insulated support layer.

1 8. An electrographic surface assembly as claimed in Claim  
2 7 CHARACTERIZED IN THAT the insulative support layer comprises  
3 a transparent substrate.

1 9. An electrographic surface assembly as claimed in Claim  
2 7 CHARACTERIZED IN THAT the resistive layer has a non-uniform  
3 surface resistance.

1 10. An electrographic surface assembly as claimed in  
2 Claim 7 CHARACTERIZED IN THAT the first cable (28, 30, 34; 128)  
3 is coupled to the resistive layer, the first cable  
4 (28, 30, 34; 128) having signal lines for communications between  
5 the resistive layer and the controller (35).

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1 11. An electrographic surface assembly as claimed in  
2 Claim 1 CHARACTERIZED IN THAT the first cable (28,30,34;128)  
3 comprises a flex cable having a printed circuit comprising a  
4 plurality of signal lines.

1 12. An electrographic surface assembly as claimed in  
2 Claim 1 CHARACTERIZED IN THAT the first cable (28,30,34;128)  
3 is a flex cable.

1 13. A method for manufacturing an electrographic surface  
2 assembly CHARACTERIZED BY the steps of:

3 providing an electrographic surface (22), a flex cable  
4 (28,30,34;128) having signal lines for communications with a  
5 controller (35) located remotely on a main electronics board  
6 and a memory device (32) capable of communications with the  
7 controller (35);

8 attaching the flex cable (28,30,34;128) to the electro-  
9 graphic surface (22);

0 attaching the memory device (32) to the flex cable  
1 (28,30,34;128);

2 generating calibration data for the electrographic sur-  
3 face (22); and

4 storing the calibration data in the memory device (32).

1 14. A method for manufacturing an electrographic surface  
2 assembly as claimed in Claim 13 CHARACTERIZED IN THAT:

3 the providing step further includes the step of providing  
4 an interface board (30); and

5 the step of attaching the memory device (32) to the flex  
6 cable (28,30,34) includes the steps of mounting the memory



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device (32) on the interface board (30) and attaching the interface board (30) to the flex cable (28,30,34).

15. A method for manufacturing an electrographic surface assembly as claimed in Claim 13 CHARACTERIZED IN THAT the step of attaching the memory device (32) to the flex cable (28,30,34;128) includes the step of mounting the memory device (32) directly on the flex cable (128).

16. An electrographic system, comprising:  
a position indicating member (14);  
an electrographic surface (22) providing possibly non-linear position indications of the member (14);  
a main electronics board (36);  
a controller (35), located on the main electronics board (36), for providing compensated position indications of the member (14);  
a first cable (28,30,34;128) connected between the electrographic surface (22) and the controller (35) and having signal lines for communications between the electrographic surface (22) and the controller (35); and  
a memory device (32) having calibration data which the controller (35) accesses to compensate for the non-linearity of the electrographic surface (22) in providing compensated position indications of the member (14); CHARACTERIZED BY:  
the memory device being integrally coupled to the first cable (28,30,34;128) and remotely located with respect to the main electronics board (36).

17. An electrographic system as claimed in Claim 16 further CHARACTERIZED BY an interface board (30) integrally

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connected to the first cable (28,30,34;128), the interface board (30) having signal lines for communications between the electrographic surface (22) and the controller (35), the interface board (30) having the memory device (32) mounted thereon, and the interface board (30) having additional signal lines for communications between the memory device (32) and the controller (35).

18. An electrographic system as claimed in Claim 17 further CHARACTERIZED BY a second cable (34) connected to the interface board (30), the second cable (34) having signal lines for communications with the controller (35).

19. An electrographic system as claimed in Claim 16 CHARACTERIZED IN THAT the memory device (32) is directly mounted on the first cable (128), the first cable (128) having additional signal lines for communications between the memory device (32) and the controller (35).

20. An electrographic system as claimed in Claim 16 CHARACTERIZED IN THAT the memory device (32) is a read only memory device (32).

21. An electrographic system as claimed in Claim 20 CHARACTERIZED IN THAT the memory device (32) is a programmable read only memory device (32).

22. An electrographic system as claimed in Claim 16 CHARACTERIZED IN THAT the electrographic surface (22) includes a resistive layer on an insulated support layer.



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1 23. An electrographic system as claimed in Claim 22  
2 CHARACTERIZED IN THAT the insulative support layer comprises a  
3 transparent substrate.

1 24. An electrographic system as claimed in Claim 22  
2 CHARACTERIZED IN THAT the resistive layer has a non-uniform  
3 surface resistance.

1 25. An electrographic system as claimed in Claim 22  
2 CHARACTERIZED IN THAT the first cable (28,30,34;128) is cou-  
3 pled to the resistive layer, the first cable (28,30,34;128)  
4 having signal lines for communications between the resistive  
5 layer and the controller (35).

1 26. An electrographic system as claimed in Claim 16  
2 CHARACTERIZED IN THAT the first cable (28,30,34;128) comprises  
3 a flex cable having a printed circuit comprising a plurality  
4 of signal lines.

1 27. An electrographic system as claimed in Claim 16  
2 CHARACTERIZED IN THAT the first cable (28,30,34;128) is a flex  
3 cable.



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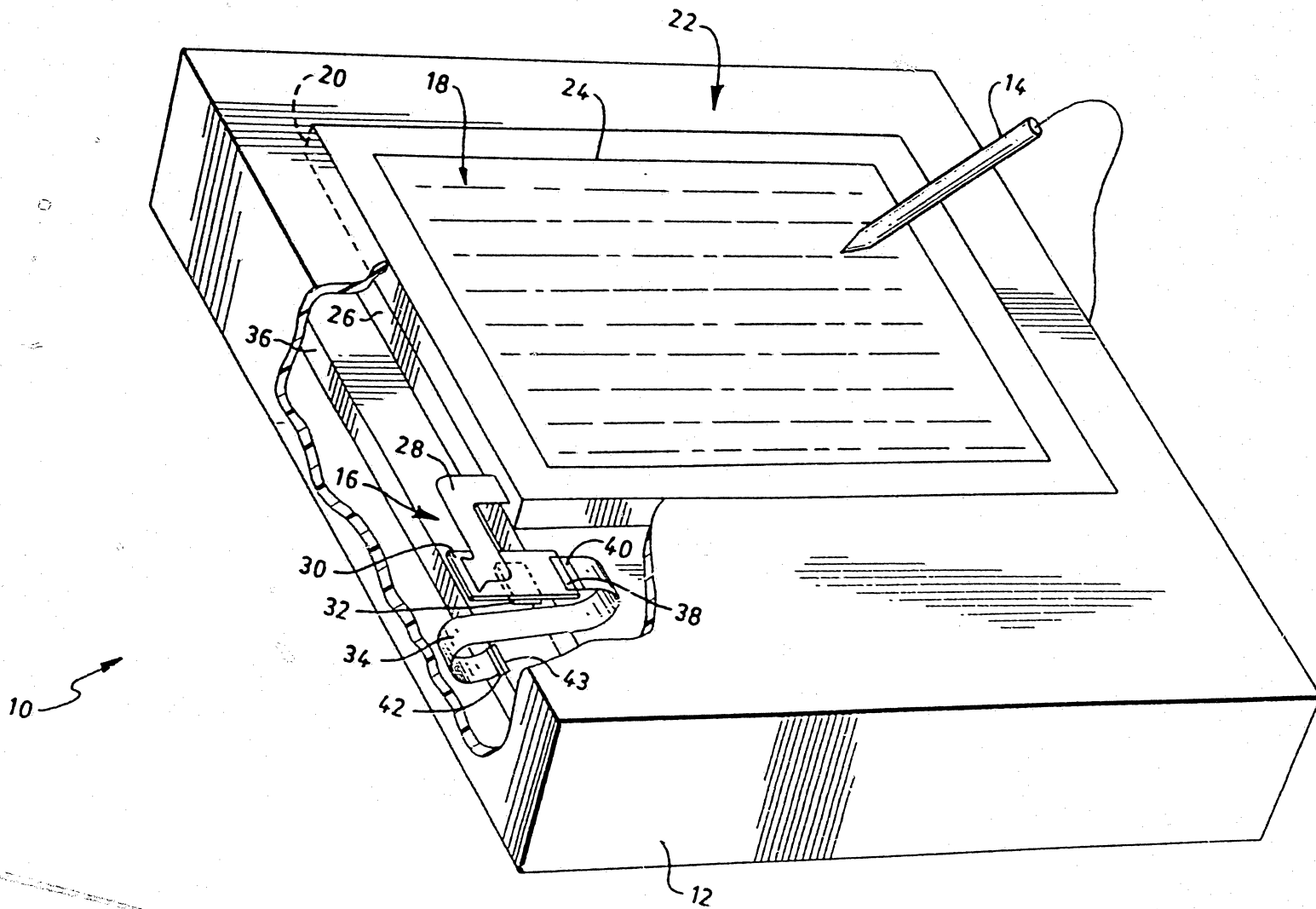


FIG. 1

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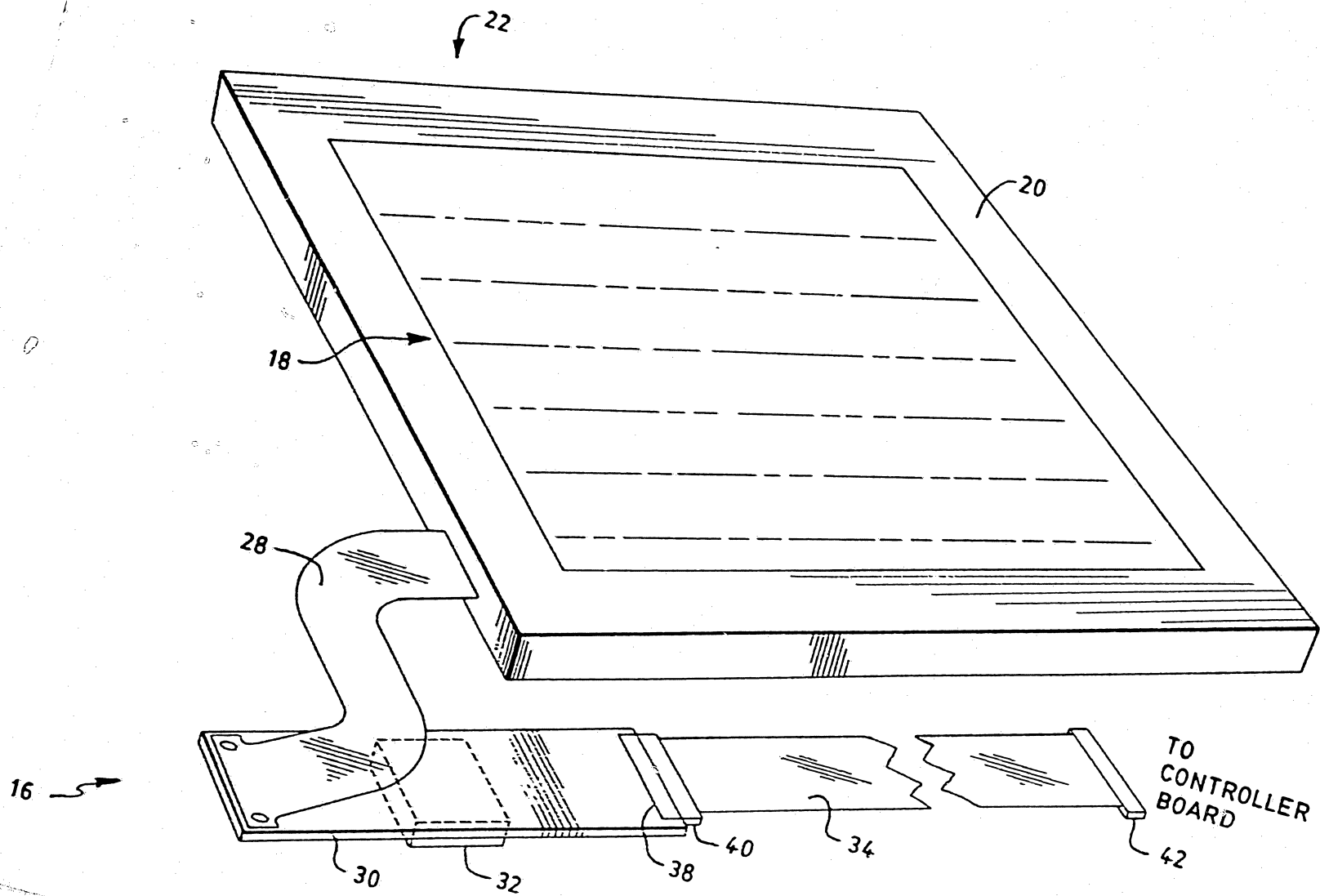
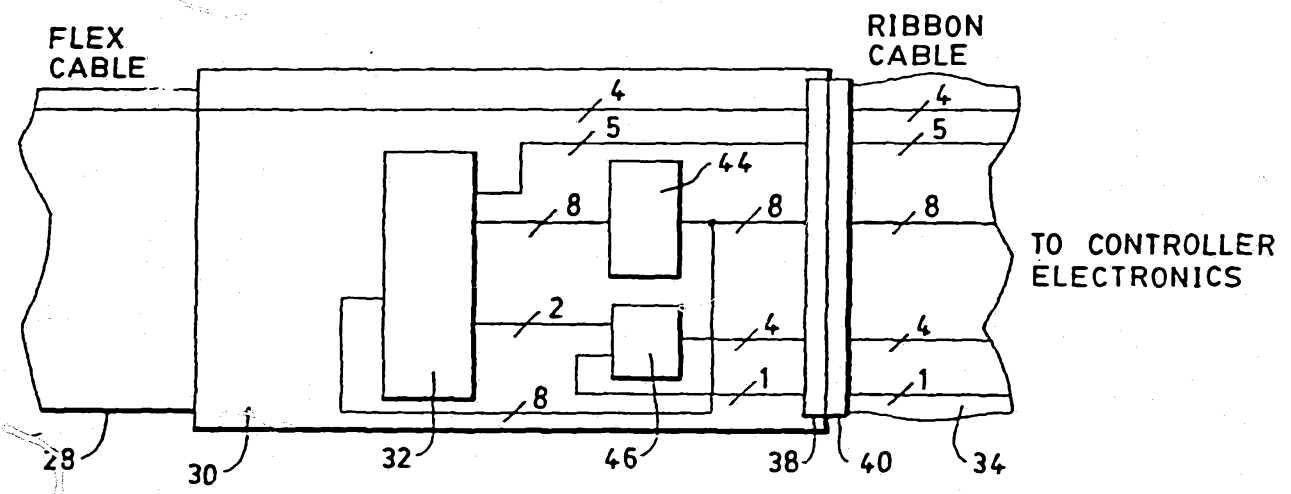
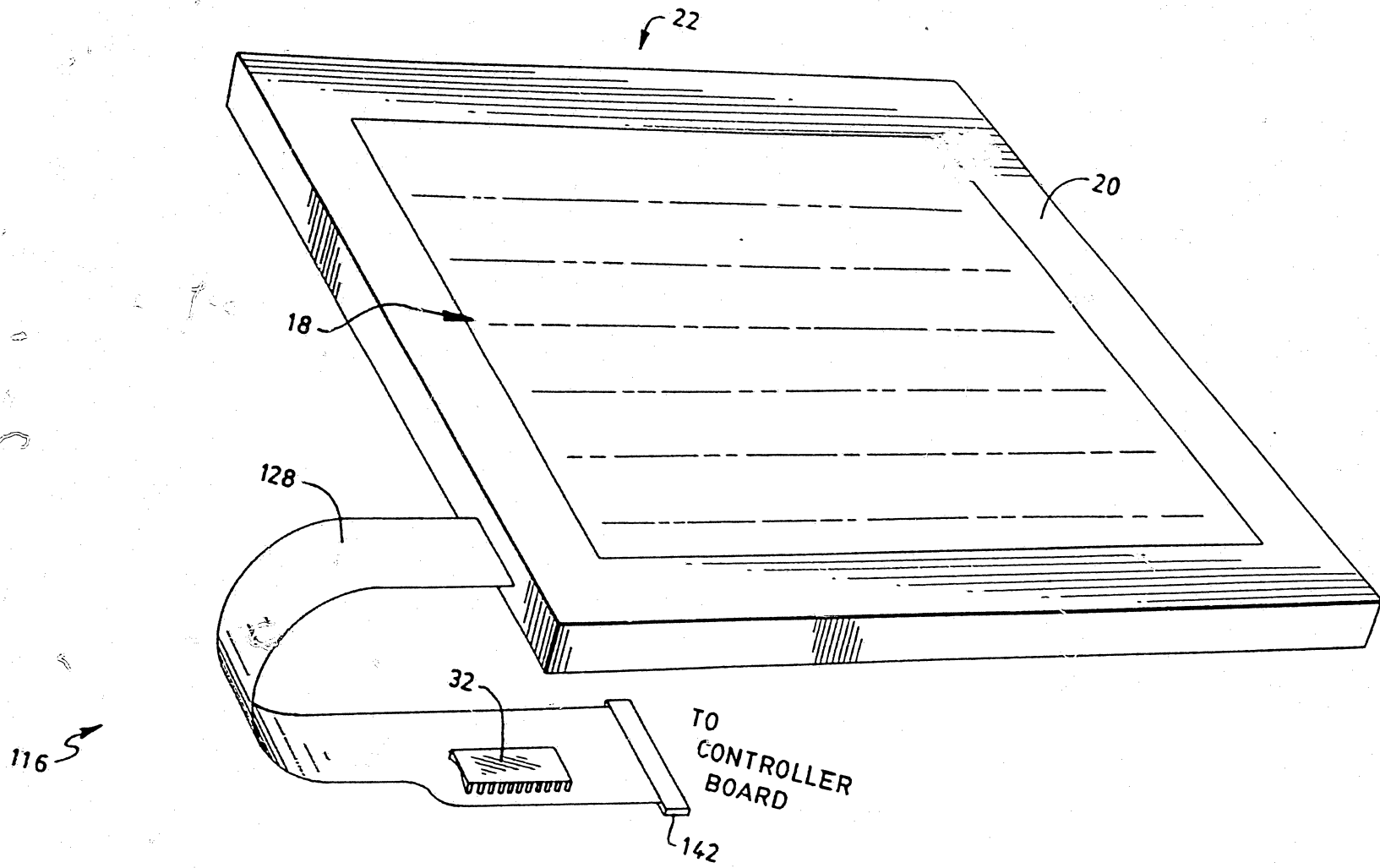


FIG. 2



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FIG. 3



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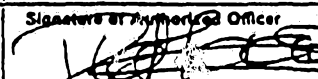
FIG. 4

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12522/92

# INTERNATIONAL SEARCH REPORT

International Application No

PCT/US 91/07898

<b>I. CLASSIFICATION OF SUBJECT MATTER</b> (If several classification symbols apply, indicate all) *		
According to International Patent Classification (IPC), or to both National Classification and IPC		
IPC <sup>5</sup> : G 06 K 11/06, G 06 F 3/033, G 08 C 21/00		
<b>II. FIELDS SEARCHED</b>		
Minimum Documentation Searched †		
Classification System †	Classification Symbols	
IPC <sup>5</sup>	G 06 K, G 06 F, G 08 C	
Documentation Searched other than Minimum Documentation to the Extent that such Documents are Included in the Fields Searched *		
<b>III. DOCUMENTS CONSIDERED TO BE RELEVANT*</b>		
Category *	Citation of Document, † with Indication, where appropriate, of the relevant passages ‡	Relevant to Claim No. †
X	US, A, 4 600 807 (KABLE) 15 July 1986 (15.07.86), see the whole document, especially claims 1,10; column 22, line 10; column 23, line 17; fig. 4.	1, 2, 9, 10, 11, 12, 16
A	--	15, 19
X	EP, A2, 0 341 190 (SCRIPTEL) 08 November 1989 (08.11.89), see abstract; claim 1; fig. 1-6.	1, 10, 11, 16
A	--	15, 19
X	DE, A1, 3 726 208 (MITSUBISHI) 07 April 1988 (07.04.88), see column 3, line 45 - column 4, line 12; fig. 1.	1, 10, 11, 16
A	--	15, 19
<p>* Special categories of cited documents: †</p> <p>"A" document defining the general state of the art which is not considered to be of particular relevance</p> <p>"E" earlier document but published on or after the international filing date</p> <p>"L" document which may throw doubts on priority claim(s) or which is cited to establish the publication date of another citation or other special reason (as specified)</p> <p>"O" document referring to an oral disclosure, use, exhibition or other means</p> <p>"P" document published prior to the international filing date but later than the priority date claimed</p> <p>"T" later document published after the international filing date or priority date and not in conflict with the application but cited to understand the principle or theory underlying the invention</p> <p>"X" document of particular relevance; the claimed invention cannot be considered novel or cannot be considered to involve an inventive step</p> <p>"Y" document of particular relevance; the claimed invention cannot be considered to involve an inventive step when the document is combined with one or more other such documents, such combination being obvious to a person skilled in the art.</p> <p>"Z" document member of the same patent family</p>		
<b>IV. CERTIFICATION</b>		
Date of the Actual Completion of the International Search		Date of Mailing of this International Search Report
15 May 1992		10. 06. 92
International Searching Authority		Signature of Authorizing Officer
EUROPEAN PATENT OFFICE		 Danielle van der Haas

III. DOCUMENTS CONSIDERED TO BE RELEVANT (CONTINUED FROM THE SECOND SHEET)		
Category *	Citation of Document, with indication, where appropriate, of the relevant passages	Relevant to Claim No.
A	US, A, 4 645 869 (ROCKWELL) 24 February 1987 (24.02.87), see claim 1; fig. 1-4.	1, 10, 11, 16
A	EP, A2, 0 306 254 (SEIKO) 08 March 1989 (08.03.89), see claims 1-5; fig. 1.	1, 10, 11, 16

In Recherchenbericht  
angeführtes Patentedokument  
Patent document cited  
in search report  
Document de brevet cité  
dans le rapport de recherche

Datum der  
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Mitglied(er) der  
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famille de brevets

Datum der  
Veröffentlichung  
Publication  
date  
Date de  
publication

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EP A2 341190	08-11-89	EP A3 341190 US A 4853493	22-11-90 01-08-89
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EP A2 306254	08-03-89	EP A3 306254 JP A2 1059529 JP A2 1059528	21-03-90 07-03-89 07-03-89
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